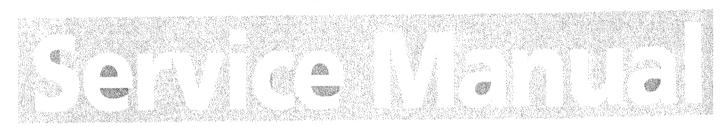
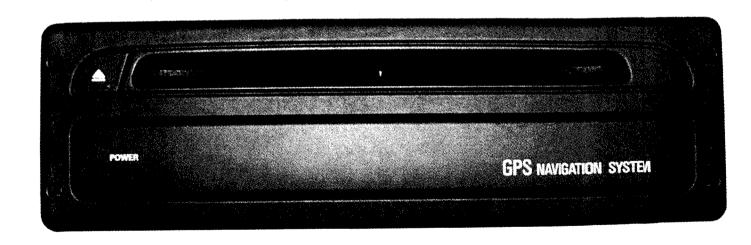


# 22SY591/23/23S BMW CC-Mk3 Navigation System



12 V →



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### **WARNINGS**



DANGER



### **ATTENTION**

Static Sensitive Devices Handle Only at Static Safe Work Stations.

Invisible Laser radiation when open.

AVOID DIRECT EXPOSURE TO BEAM

#### **Documents**

Beside this documentation the following information sources are available for service purposes:

- The service partslists 22SY591/23 & -/23S in the SAP System of Philips Consumer Service Eindhoven
- The service manual for the CD ROM drive CDM-M2/2.3, order codes 4822 725 25483 & 4822 725 25491
- The service manual for the CD ROM drive CDM-M2/8.3, order codes 4822 725 25491
- The service manual for the CC-Mk3 Test Software, order code 4822 725 25495
- The 'Instructions for Use', prepared by BMW

### **Navigation system BMW**

The Navigation System for BMW consists of the following parts:

- The navigation computer 22SY591/23 (hor. mounting) or 22SY591/23S (vert. mounting)
- The GPS antenna 22SY553/70
- The display, audio circuits, speakers and controls integrated in BMW car are controlled by the analogue outputs and I/K (BMW communication) bus
- The interface cable set

### **General specification**

Ambient temperature	(functional)	-20 / +70°C
Ambient temperature	(full specification)	-15 / +55°C
Supply voltage	(SPIL functional)	6 - 16V
Supply voltage	(full specification)	9 - 16V
	(CD not operating)	6 - 9V min.
		0.5A typical
	3	<2A peak
Quiescent current of BN	MW CC-Mk3 navigation system	<100µA typical
		under S/W control
-		22W at $4\Omega$ at loudspeaker output;
, (ddio odtpala (1919),		$3V_{\rm eff}$ on voice out line – at $470\Omega$
Voice S/N ratio		≥ 50dB (A-weighted)
		R / G / B output (50 $\Omega$ / 75 $\Omega$ )
•		≥ 40dB
	unit)	1.5kg
_		179.6 x 158.2 x 51.6 mm (W x D x H)
-		Not applicable for CC-Mk3 BMW computer
Supply voltage Supply current of BMW	(CD not operating) CC-Mk3 navigation system  WW CC-Mk3 navigation system  e  unit)	6 - 9V min.  0.5A typical  <2A peak  <100µA typical  under S/W control  22W at 4Ω at loudspeaker output;  3V <sub>eff</sub> on voice out line – at 470Ω  ≥ 50dB (A-weighted)  R / G / B output (50Ω / 75Ω)  ≥ 40dB  1.5kg  179.6 x 158.2 x 51.6 mm (W x D x H)

## Signal description

#### Blue MQS Connector (X1)

	X1-1	Battery+	(KL30) +12V permanent.
	X1-2	Not connected	
	X1-3	I/K bus	BMW communication bus (I/O bus; positive edges are used to switch on the CC-Mk3 (CSI-ON signal)
	X1-4	RED75 *)	Red output line of RGB analogue video at $75\Omega$ interface
	X1-5	GREEN50 +SYNC	Green with sync output line of RGB analogue video at $50\Omega$ interface
5	X1-6	BLUE50	Blue output line of RGB analogue video at $50\Omega$ interface
2	X1-7	RED50	Red output line of RGB analogue video at $50\Omega$ interface
1			

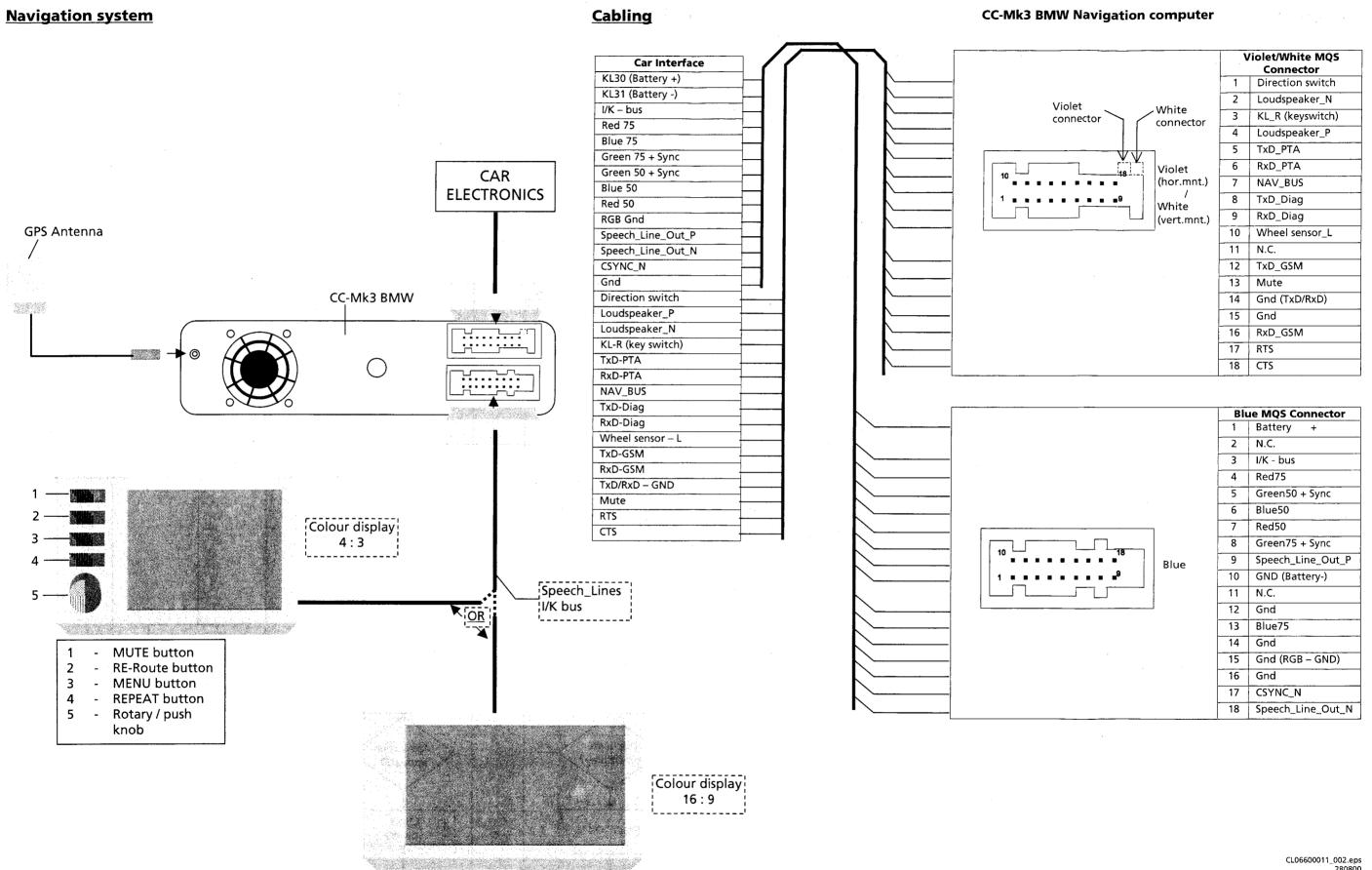
PC 1

X1-8	GREEN75 +SYNC *)	Green with sync output line of RGB analogue video at $75\Omega$ interface
X1-9	SPEECH_LINE_OUT_P	Audio line out positive
X1-10	GND (KL31)	Battery ground
X1-11	Not connected	
X1-12	GND	
X1-13	BLUE75 *)	Blue output line of RGB analogue video at $75\Omega$ interface
X1-14	GND	
X1-15	RGB GND	
X1-16	GND	
X1-17	CSYNC_N	Composite sync output (TTL level) – active low – load $\geq 1k\Omega$
X1-18	SPEECH_LINE_OUT_N	Audio line out negative
*)	The 750 RGR output is	derived by adding a resistor to the 500 interface at the connector block:

\*) The  $75\Omega$  RGB output is derived by adding a resistor to the  $50\Omega$  interface at the connector block; Only one of the interfaces (either  $50\Omega$  or  $75\Omega$ ) may be used!

### Violet (-/23) or White (-/23S) MQS Connector (X2)

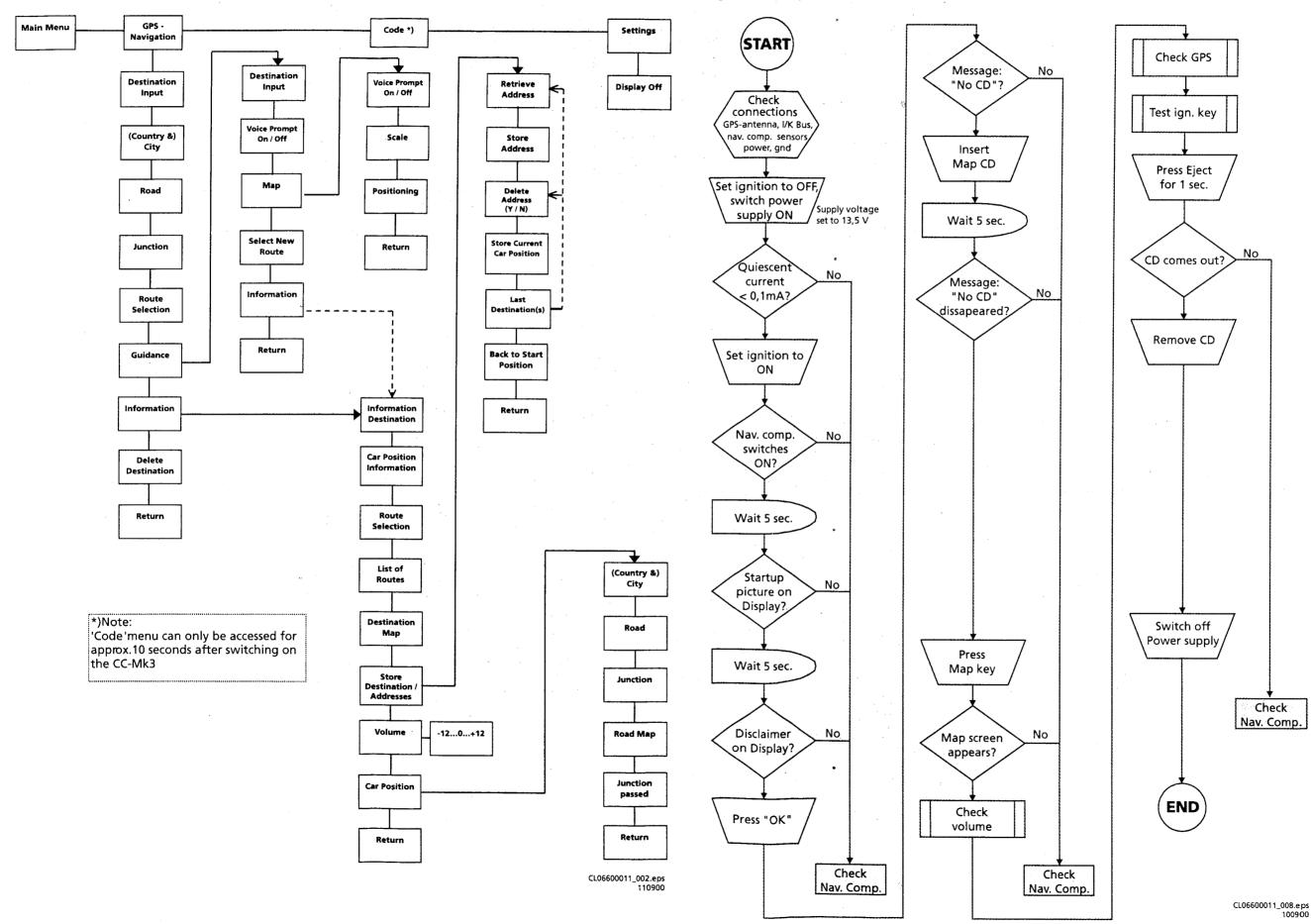
X2-1	Direction switch	Detects whether vehicle moves either forward or backward
X2-2	Loudspeaker_N	Negative loudspeaker output; max. output 22W at $4\Omega$ (between X2-2 and X2-4)
X2-3	Keyswitch	(KL-R) +12V switched (12V=keyswitch ON / 0V=keyswitch OFF)
X2-4	Loudspeaker_P	Positive loudspeaker output
X2-5	TxD-PTA	Transmitted data (RS-232C output) via CSI board – Remark: to be defined yet
X2-6	RxD-PTA	Received data (RS-232C input) via CSI board – Remark: to be defined yet
X2-7	NAV-BUS	I/O communication to the display unit; the bus is based on the I_bus concept
X2-8	TxD-DIAG	Transmitted data (output) via RS-232C interface
X2-9	RxD-DIAG	Received data (input) via RS-232C interface
X2-10	Wheel-sensor-L	Input of wheel sensor left signal from ABS (Anti-Block-System) computer
X2-11	Not connected	
X2-12	TxD-GSM	Transmitted data (output) via CSI board – for GSM purposes – Remark: to be defined yet
X2-13	(B)MUTE_N	Mute output to radio set; active low, passive high (floating); $R_{\mbox{\tiny load}}$ > $10 k\Omega$
X2-14	RS232C-GND	RS232C serial interface ground.
X2-15	GND	
X2-16	RxD-GSM	Received data (input) via CSI board – for GSM purposes – Remark: to be defined yet
X2-17	RTS	Ready-To-Send output (CSI board)
X2-18	CTS	Clear-To-Send input (CSI board)



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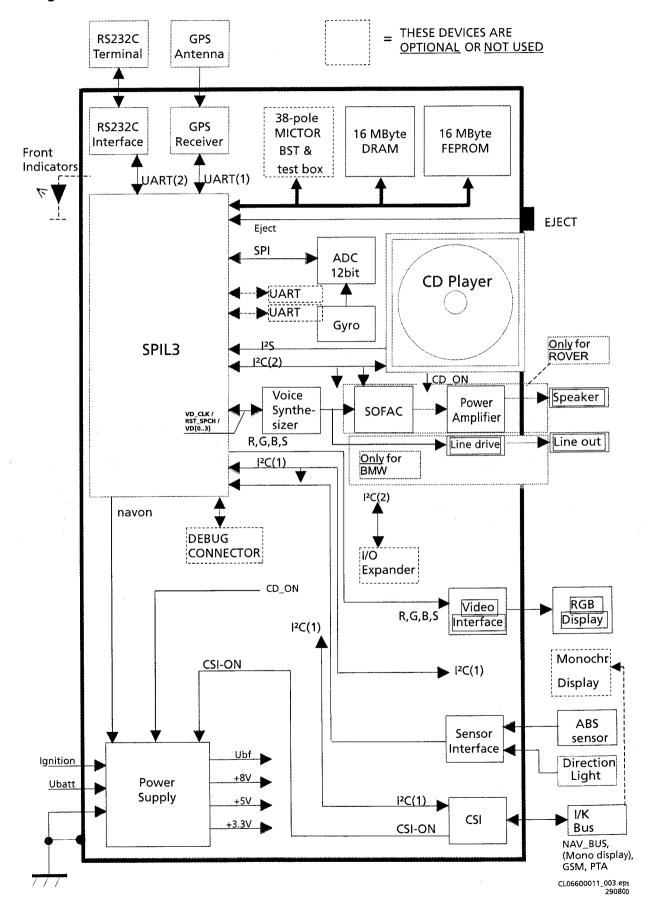
### **Menu survey**

## System test - Flow chart



## Navigation computer 225Y591/23/23S

#### **Block diagram**



### **Block diagram description**

#### SPIL3A

The SPIL3A is an "ASIC"; that is the core of the CC-Mk3 navigation computer. It has the following main functions:

- CPU
- RAM, FEPROM and BUS controller
- External bus interface EBIF
- CD, I<sup>2</sup>C (2x), I<sup>2</sup>S and Interrupt Request Controller
- Colour Graphics display controller
- Video DAC (not used here)
- CD data retrieval controller
- Voice synthesizer interface
- Serial communication interfaces (4 UART's and Serial Peripheral Interface controller)
- Clock and Reset signals generator
- Timers and Compare units
- General I/O (16x)
- RC5- and BST interface (not used here)
- Address protection unit

The CPU inside the SPIL3A is running on 54MHz and has 4kByte instruction cache, 1kByte data cache, 8kByte internal SRAM, multiply accumulate unit and an "on-chip" debug support. This CPU has a non-multiplexed 32 bit data and address bus which is used in the asynchronous mode.

The clock frequency (54MHz) is derived from a 6.75MHz oscillator.

The main tasks of the processor are:

- Dead reckoning
- Map matching
- Route planning
- Route guiding
- Voice output
- Display processing
- Map database handling
- Diagnostic
- User Interface
- Communication via I<sup>2</sup>C, I<sup>2</sup>S and I/K busses.

#### **FEPROM**

The size of the instruction memory is 16MByte. It has a 32 bit data bus and is controlled by the SPIL3A.

All sectors of the FEPROM can be locked (disabled for erasing and writing) under software control. Additionally, there is a general write protection pin, which is also controlled by software.

At (power-on) reset the CPU starts from the FEPROM to boot up. The sector dedicated to the boot SW in the FEPROMs is programmed with the application software from CD. A so-called 'SW loading flag' is set when the application s/w must be loaded and this flag is reset by this s/w after it is loaded properly into FEPROMs.

Note: the TEST software uses the DRAM instead of the FEPROMs.

FEPROMs are also used for non-volatile data storage. The non-volatile storage is intended for the following data:

- Application (calibration, last car position, address book, etc.)
- Identification
- HW test bytes
- Statistical data (a.o. time of operation)
- Error logging
- Customer specific data
- VDO / Philips ID block
- Software loading block
- Voice data.

#### DRAM

The size is 16MByte. The data memory has a 32 bit data bus and is controlled by the SPIL3A. It is used for the user specific data and for loading the TEST software from CD.

#### Voice synthesizer

The voice synthesizer is based on the MSM6585. The chip is used in 4bit ADPCM mode. Sample frequency is 16kHz; bandwidth is 6,800Hz. The output of the synthesizer chip is filtered and fed to a SOFAC. The voice data normally is stored in FEPROM, but DRAM too can be used as voice data file. The voice synthesizer control, to transfer data from the voice data file to the voice synthesizer, is part of the SPIL3A.

#### SOFAC, Audio line driver and power amplifier

The SOFAC is based on the TDA7342. The SOFAC control bus is the I<sup>2</sup>C(2) bus of the SPIL3A. Input channel 1 consists of the CDA-R and CDA-L outputs. Channel M is the filtered voice output VOI-SIG.

With the SOFAC a modification of the volume, bass and treble of the audio signal delivered by the voice synthesizer can be done.

The output left front of the SOFAC is fed to a power amplifier stage (TDA7375) (used for BMW-Rover) and with an attenuation of –24dB it is fed to a TDA7052 line driver (used esp. for BMW) as the "voice line out" signal. The "voice line out" signal (Speech\_Line\_Out\_P / N esp. for BMW) is used to control the audio circuit in the display unit.

At 0dB the amplifier is able to deliver a maximum of 22W with a load of  $4\Omega$  and  $3V_{eff}$  on the "voice line output". The outputs "left rear" and "right rear" are fed to line drivers as the "line out left" and "line out right" signals (Speech Line Out P/N for (BMW-)Rover). The output levels are  $1V_{eff}$  at 0dB.

The output of the power amplifier is connected to a loudspeaker inside the car.

#### Gyro

The gyro is an angular rate sensor. The dynamic range is -80 °/sec to +80 °/sec. The bandwidth is 7Hz. The gyro is supplied with a +5Vgyro, derived from the +Ubfs (filtered and switched car battery voltage). Electrical specs.:

Symbol	Parameter	Min.	Тур.	Max	<u>Unit</u>
Vcc	Supply voltage	4.75	5.0	5.25	V
lcc	Supply current @Vcc=5V	8		14	mA
Vol	Output voltage low			0.5	V
Voh	Output voltage high (Vcc-0.15V)	4.6	4.85		V
	Zero point output	2.1	2.5	2.9	V
	Temperature range	-30		+80	°C
	Sensitivity		25		mV/deg/sec
	Temperature drift (-30+80°C)	-50		+50	mV/7.5°C
	Noise			10	mVpp

#### **Gyro interface / ADC**

For sufficient navigation accuracy, the gyro signal has to be converted to 12bit digital data.

The gyro output signal is filtered with a low pass filter and is converted from analog to digital data via a 12bit ADC. The supply and the reference voltage of this ADC are connected to +5Vgyro for minimum ADC conversion errors. The ADC is controlled and read by the SPIL3A via a SPI – bus. An extra multiplexer is used to increase the number of channels from 8 to 16. The other channels of the ADC are used for diagnose purposes:

- Temperature (sensor on main board, used for temperature dependent fan control).
- +U<sub>batt</sub>
- Reference voltage (1.25V)
- Internal supply voltages of the mainboard (+3.3V<sub>nav</sub>, +5V<sub>nav</sub>, +8V, +5V<sub>v</sub> and +5V<sub>r</sub>)
- Several audio diagnosis stages
- Display driver (RGB and CSYNC outputs)
- Fan current
- Voice line out
- CSYNC buffered output

#### **GPS** receiver

The GPS receiver is supplied with 5V and a backup supply (3.2V...5.25V, 2µA typ.) to supply the SRAM.

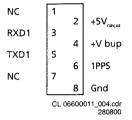
Nominal data rate is 9600 baud. Communication is established via an UART channel.

Cold start: the typical time to first fix (initial position) is less than two minutes. To build up a complete almanac, the receiver will need 15 minutes.

Warm start (power down for more than 60 minutes): time to first fix is less than 45 seconds.

Hot start (power down for less than 60 minutes): time to first fix is less than 20 seconds.

GPS connector, 8pin micromatch, pos. 1004:



#### Sensor input interface

The interface between the input signals (ABS sensor and direction light (reverse gear)) and the SPIL3A is called "sensor input circuit". It contains hardware for filtering, amplification and level shifting.

Connectable are "open collector" circuits or other active switches and inductive sensors.

The 'Direction Light' input works with rectangular "active high" signals with the following electrical specifications:

Symbol	Parameter	Min.	Тур.	Max	Unit
V <sub>T</sub>	Negative going threshold light / acc input	3.2	3.3	3.4	V
$V_{\tau_{+}}$	Positive going threshold light / acc input	3.9	4.1	4.3	V
V <sub>T</sub>	Negative going threshold direction input	3.2	3.3	3.4	V
V <sub>1+</sub>	Positive going threshold direction input	3.7	3.8	3.9	V
I, 'T	Input current low @ Ui = 0V		0.001	0.01	mA
. I <u>.</u>	Input current high @ Ui = 16V		0.75		mΑ
Ć.,	Input capacitance @1MHz		4700		pF
t	Pulse width	2			msec
Ű,	Absolute max, during 60 sec.	-100		+75	V
<b>l</b> , '	Absolute max, during 60 sec.	-0.05		+3	mA

The ABS sensor input is adapted to the customer requirement by a special interface which is placed inside the head unit. The output of this interface leads to the standard circuitry on the main board. The ABS sensor input can work with rectangular signals with high levels of above 3.3V.

Electrical specifications of "ABS sensor" signals:

Symbol	Parameter	Min	Тур.	Max	<u>Unit</u>
V <sub>T</sub>	Input threshold voltage (p-to-p / 10Hz)	250	300	380	mV
V <sub>T</sub>	Input threshold voltage (bypass)	1.7		3.3	V
C <sub>in</sub>	Input capacitance @1MHz	900	1000	1100	рF
I, ""	Input current low @ Ui = 0V		0.9	1.1	mA
ايّ	Input current high @ Ui = U <sub>bat</sub>		0.001	0.01	mΑ
F.	Frequency range			3	kHz
	Duty cycle range	1		99	%
U,	Absolute max, during 60 sec.	-60		+110	V
l,	Absolute max, during 60 sec.	-3		+0.05	mA

#### CD player / unit

The CD player is derived from the CD player audio module and adapted for double speed and a buffered I<sup>2</sup>S bus, available on the CD player connector. The CD module used in the BMW CC-Mk3 22SY591/23 is the CDM-M2/2.3 version for horizontal mounting, whereas the 22SY591/23S uses the CDM-M2/8.3 module. Refer to the CDM-M2/2.3 and –8.3 Service – and Supplement manuals for more information of this CD modules. Interface between CD player and main board:

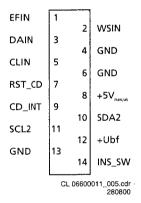
- I2C(2) control bus
- I2S data bus (WSIN, DAIN, CLIN, EFIN)
- Supply (+5V, +12V, GND)
- Reset
- CRQ for an I<sup>2</sup>C communication request (active low)
- INS\_SW to switch on the CC-Mk3 in case a CD will be inserted (active low).

The specified CD player operating temperature range is -20°C / +70°C.

The CD player is a double speed CD-drive with an access time of ~ 500 ms typical, 1.5 sec maximum.

The CD player can also run in single speed. The current implementation of CD player only supports single session discs.

CD player connector, 14pin micromatch, pos. 1002:



#### **CD** control interface

The CD player control bus is I<sup>2</sup>C. It has an additional 3rd line, CRQ (Communication ReQuest, CD\_INT named in the connector diagram). The CD player is always slave.

The CRQ line is set low to signal the SPIL3A that information is available.

The CD player reset input is controlled via an general purpose output of the SPIL3A.

#### **CD** data interface

The data interface on the CD player uses the Philips I2S format.

It consists of 4 output signals from the EFM decoder:

- WSIN = Word Select
- DAIN = Data
- CLIN = Data Clock
- EFIN = Error Flag.

The data interface from CD player (I2S bus) to host memory (DRAM) is fully covered by the SPIL3A.

#### **Power supply**

The power supply generates and stabilizes the supply voltages for the navigation computer. It is supplied from the car battery and is switched on either the ignition signal or via the I/K bus signal ("CSI-ON") from the CSI board. Switching on by the 'Eject' button or by inserting a CD is provided as well. The switching off can be delayed by the SPIL3A. The unit is switched off after a software controllable time when the ignition signal is off and if there is no I/K bus activity ("CSI-ON" is low).

The main functions of the supply unit are:

- Supply of all digital and analogue circuitries on the mainboard, gyro, GPS receiver and CD player
- Detection of power failure on the +3.3V supply for the host computer and +5V
- Power-on / -off control
- Supply of a cooling fan

#### Specification:

- Switch mode power supply, down converter.

Input voltage range is 6V to 16V. If the supply voltage from the battery is 9 - 16V, the navigation computer is in normal operation. Between 6 - 9V the computer has no power failure, but is partly operational (supply voltage too low to drive the disc motor and other motors properly).

Efficiency 85% at 12V input voltage, 70% at 5,5V input voltage.

Two SMPS generate 5V and 3.3V. Maximum load is 1A per supply.

+5V series low drop regulator for the Gyro.

- Power-on/-off switch by means of a high side switch.

The minimum battery voltage to be able to switch on the navigation computer supply is +6.0V.

A high level, Ui > 3.3V, on the ignition – or CSI input will switch on the high side switch. The high side switch can also be switched on via the 'Eject' button to allow CD exchange when the button is pushed again. The power is also switched on in case a CD is inserted.

When the ignition – or CSI signal is low, the power can be kept on by the SPIL3A as long as it supplies "power-off delay" pulses to the power supply.

The pulse rate must be  $\geq$  30 Hz. The minimum pulse duration is 100 msec. The power supply is switched off when the ignition signal is low, the "power-off delay" pulses are stopped and if there is no I/K bus activity. The CC-Mk3 can be switched on via positive edges at the I/K-bus signals (CSI-ON is active).

- Quiescent current (power switched off) is <100µA typ.

- Typical supply currents:

Gyro:

+5V / 20mA (from +Ubfs)

CD player:

+5V / 200 mA (during play) +Ubfs / 100mA (lp = 1A)

NAV:

+3.3V / 350mA

+5V / 300 mA (digital circuits) +8.0V / 30mA (analogue circuits)

CSI board:

+5.0V / 150mA

GPS w/ant.:

+5.0V / 120mA typ.

Fan:

+Ubfs / 70mA

- Power failure threshold voltage

Power failure threshold voltage = 3.0V for +3.3V supply.

Power failure threshold voltage = 4.7V for the +5V supply.

Both the +5V and the +3.3V supply must be above the threshold voltage before the PWFN signal is released (high) with an extra delay time of 6msec. If one of the voltages goes below the threshold voltage, the PWFN signal is activated (low) again. The host reset signal (RST\_HOSTN) is an active low output signal from the SPIL3A. The signal is low when PWFN is low and is set high when the program execution starts. The output RST\_HOSTN is set or reset under software control.

#### **Eject**

The 'Eject' button is connected tot a general purpose I/O of the SPIL3A. The SPIL3A controls the CD player loader functions, including eject. When the CC-Mk3 power is off, the eject button switches on the power. When the button is pushed again, the CD is ejected by SPIL3A control. The power is switched off after power-down delay time, when the ignition signal is off, when there is no I/K bus activity and when the eject button is released.

#### Cooling

Application of a forced cooling by floating air is necessary. For this a cooling fan on the rear bracket is applied, mounted with 4 fan strings. For low fan temperature and controlled airflow the fan is at the air intake side.

The air outlets are in the top cover. To protect the CD player laser and CD-disc against dust, the CD player unit is shielded from the air flow by a dust shield. A dust shutter will cover the CD slot.

The fan is controlled by software. Below a temperature of +55°C (internal temp. sensor), the fan will be switched off. Above +60°C it will be switched on. The range in between is intended for on/off switching hysteresis. In case of overheating (sensor temperature is higher than +80°C), the module is put in a state, where dissipation is decreased to a minimum and the CD player is stopped and not accessed.

It is not possible to switch off the computer under software control while the ignition signal is present.

#### Colour display interface

The RGB display interface is used to drive a load of  $50\Omega$  (for each of the R, G and B signals). With help of software, it is configured to deliver a negative CSYNC-in-Green signal. The output level of the drivers is  $0.7V_{pp}$  typ. @  $50\Omega$ .

The 75 $\Omega$  RGB output is derived by adding a resistor to the 50 $\Omega$  interface at the connector block; Only <u>one</u> of the interfaces (either 50 $\Omega$  or 75 $\Omega$ ) may be used.

#### Monochrome display interface

The monochrome display interface is controlled via the NAV-BUS. This bus is an I/O bus, based on the I/K – bus.

#### **RS232 interface**

The UART2 of the SPIL3A represent an interface which is according to the RS232C standard. The adaptation of the signal line RxD and TxD is done by a RS232 transceiver LT1181.

#### **RC5** (remote control) interface

This device is not used here. Via the internal I<sup>2</sup>C interface, the CSI / CSB (<u>C</u>ustomer <u>S</u>pecific <u>I</u>nterface / <u>B</u>oard) and the I/K-bus, the CC-Mk3 is controlled by the car controls.

### **CSI / CSB (Customer Specific Interface / Board)**

The Customer Specific Interface board forms an interface between the I/K-bus and the CC-Mk3 computer. In general, the CSI board is used for the following functions:

- NAV-BUS, to control a monochrome display
- I/K bus interface
- PTA (Personal Travel Assistant) RS-232C interface
- DFÜ (Daten Fern Übertragung) RS-232C interface

The I<sup>2</sup>C(1) bus is used for control – and data transfer to and from the CSI board. This board is connected via a 30-pole male connector to the main pcb. See elsewhere in this manual for the connector layout.

#### **Optional Test connectors**

There are optional connectors on the main pcb, to connect additional measuring equipment. Here a more detailed description follows:

- I/O expander
- DEBUG connector: to connect the 'debug' bus of the SPIL3A (1x20-pole); is used only for servicing purposes. Via this connector the SPIL3A registers can be accessed directly.
- MICTOR connector: via this connector a test box can be connected to the computer to execute a BST (Boundary Scan Test) (38-pole)
- Data logger connection to the 'GPS communication and Odometer Output' (6-pole Micromatch)
- Data logger connection to the SPI for GYRO data (6-pole Micromatch)

#### **Devices not used in BMW CC-Mk3**

The following devices are not used:

- MPEG1 / Video memory
- YUV > RGB converter and multiplexer
- Audio DAC
- D<sup>2</sup>B electrical and optical bus
- RC5 interface for remote control

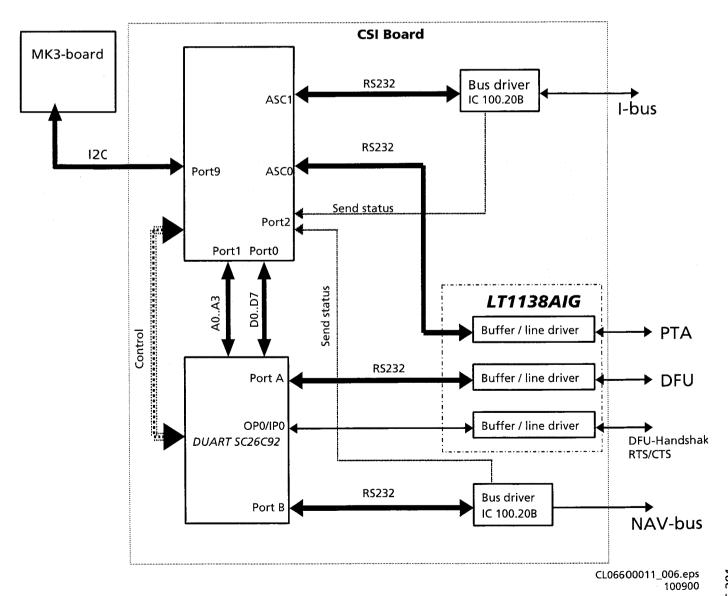
#### **Indications**

The CC-Mk3 front contains the following indications:

- Eject button is illuminated, when the power is switched on.

## **Customer Specific Board (CSB)**

#### **CSB** architecture



## **External interconnections**

Refer to the section 'Cabling' in this manual for all external interconnections.

## **CSB** connector

Pin	CSB Signal	Description / Value	
1	GND	ov	
2	TxD-GSM	RS-232C transmit - GSM purposes	
3	RxD-GSM	RS-232C receive - GSM purposes	
4	GND	ov	
5	RTS-GSM	Ready to Send (RS-232C) - GSM purposes	
6	CTS-GSM	Clear to Send (RS-232C) - GSM purposes	
7	+5V	Supply for CSI board	
8	SCL	5VDC (SPIL3A)	
9	SDA	5VDC (SPIL3A)	
10	GND	0V	
11	RST_HOSTN	3.3VDC input from navigation	
12	CSB_INT	3.3VDC output to interrupt navigation	
13	CSI_ON	5VDC output to signal 'ON' for CC-Mk3	
14	TxD-PTA	RS-232C transmit - via PTA	
15	RxD-PTA	RS-232C receive - via PTA	
16	N.C.	N.C.	
17	N.C.	N.C.	
18	GND	ov	
19	N.C.	N.C.	
20	N.C.	N.C.	
21	N.C.	N.C.	
22	N.C.	N.C.	
23	N.C.	N.C.	
24	+U <sub>bfs</sub>	Supply voltage for CSI board	
25	N.C.	N.C.	
26	N.C.	N.C.	
27	GND	ov	
28	I/K-BUS	I/O signal for I/K bus	
29	NAV-BUS	I/O signal for NAV-bus (monochrome display)	
30	N.C.	N.C.	

### **Identification block description**

### 1. Philips ID block

CARIN computer type : Mk3
Hardware ID : 0103
Active Levels : 80

#### 2. Customer ID block

Validity Check	:	Byte $0 = 1$
Customer ID for customer identification block	:	Byte $1 = 0$
Required country setting (Europe / USA)	:	Byte $2 = 0$
Required unit system (Metric / Anglo / American)	:	Byte $3 = 0$
Required date format (Europe / USA)	:	Byte $4 = 2$
Required time format (24h / 12h(AM/PM))	:	Byte 5 = 1
Required GALA mode (off / 14)	:	Byte $6 = 1$
Required hand brake mode (on / off)	:	Byte $7 = 0$
Required speed limit configuration (Europe / USA)	:	Byte $8 = 0$
Monitor (display) format (4:3 / 16:9)	:	Byte 9 $= 1$
Enable security module	:	Byte $10 = 1$

### 3. Software loading block

Customer ID for software loading : BMWC01S (colour) / BMWM01S (monochr.)
First language : d\_f.csf

Second language : d\_i.csi
Second language : gb\_f.csf

### **I/O Addresses**

The following I/O addresses are reserved for the CC-Mk3 hardware devices:

Device	I <sup>2</sup> C Bus	I/O address (hex)
CSI board	I <sup>2</sup> C (1)	54
CD player control port	I <sup>2</sup> C (2)	30
SPIL3A	I <sup>2</sup> C (1)	60
SPIL3A	I <sup>2</sup> C (2)	62
SOFAC control port	I <sup>2</sup> C (2)	88

With help of the 'Free Access' command **00000040** (refer to Test Software Service manual), these addresses can be accessed and modified directly.

#### **Checks**

#### 1. Introduction

This section describes the checks to be done after repair of the main board and/or peripheral devices.

To have an exact overview and a complete description of the tests / commands mentioned here, refer to the CC-Mk3 TSW Service manual. Figure i shows the TSW test connections, whereas figure ii shows the CC-Mk3 in its total (BMW) measuring environment.

#### 2. Preparing 'Burn-In' Loop test

- To start the 'Burn-In' Loop test, connect U<sub>bat</sub> to pins 1001-1 (KL-30) and 1000-3 (KL-R), GND to pin 1001-10 (KL-31) and connect all peripherals as shown in the figures.
- The following tests must be programmed into the 'Burn-In' loop:

0430000019

TCU

0430000065

IRC1

0430000066

IRC2

0430000069

Timer0

0430000018

Bus error

0430000024

APU

0430000027

DRAM march

0430000030

Mirroring

0420000001

I<sup>2</sup>C1 bus

0A30000B12000000020201

CD read, 2x,

double speed,

00 for APM

7112 235 1402

01 for Carin II

rel. 1.2 CD

093000B212000000102

Access time,

1x double speed

043000004C ADC

043000004A Fan

SRAM March 0420000012

0430000046

Video \*

0420000032

Counter \*

043000001A

GPIO \*

0420001000

Loudspeaker \*\*

0420001001

Audio line driver out \*\*\*

\* - dummy required

\*\* - only for (BMW-)Rover

\*\*\* - only for BMW

- Refer to the CC-Mk3 TSW Service manual to know how a 'Burn-In' loop test must be set
- Enter the 'Burn-In mode Next Time' command 0000001601.

#### **Burn-In test (only SSP)** 3.

- Insert the CARIN test CD.
- Wait for one hour.
- Press the 'Eject' button of the CC-Mk3 system and remove CD.
- Disconnect U<sub>bat</sub> from pin 1000-3 (KL-R) to switch off all devices.
- Keep the other power connections intact.

#### 4. Complete CC-Mk3 system tests

The tests are subdivided into:

- Get 'Burn-In' results.
- Power supply test.
- Front tests.
- CD tests.
- Interactive 'voice' test.
- GPS test.
- Fan test.

All these tests are described into more detail hereafter.

#### Get 'Burn-In' results 5.

- Connect U<sub>bat</sub> to pin 1000-3 (KL-R).
- Use TSW command 00000015.
- Check if no error is shown at the monitor.

#### 6. **Power supply test**

- Connect U<sub>bat</sub> to pin 1000-3 (KL-R).
- Use the ADC test command 3000004C.
- Check if no error is shown at the monitor.

#### 7. **Front tests**

- Connect U<sub>bat</sub> to pin 1000-3 (KL-R).
- Thereafter, check the:
  - Illumination
    - and
  - eject button.
- Check the front illumination.
- Use the TSW 'Eject button' test command 30000072 and press the 'Eject' button within 10 sec.
- Check if no error is shown at the monitor.

#### 8. **CD** tests

- With the CD tests the following items are tested:
  - CD insert
  - play (digital) data
  - CD eject
- Disconnect U<sub>bat</sub> from pin 1000-3 (KL-R).
- Insert the CARIN test CD into the slot and check if the CC-Mk3 system is switched on and the CD is loaded.
- Connect U<sub>bat</sub> to pin 1000-3 (KL-R).

- Use the TSW 'Dynamic Behaviour' test: 30000B12000000020200
   for APM 7112 235 1402
   OR
  - 30000B12000000020201

for CARIN II rel.1.2 CD .

- Check if no error is shown at the monitor.
- Use the TSW 'Eject' command 00000007.
- Check if no error is shown at the monitor and the CD is ejected without problems.
- Connect a headphone between Audio Line Out L / R and GND.
- In the headphone line <u>an external volume</u> regulator must be installed!
- Insert Audio CD SBC429.
- Use the TSW 'Voice' test command 000000902 to generate a 0dB / 1kHz output signal.
- Check if the sound in the headphone sounds properly.
- Use the TSW 'Stop' command 00000006.

#### 9. GPS test

- Connect U<sub>bat</sub> to pin 1000-3 (KL-R).
- Connect antenna to the GPS receiver.
- Use the TSW 'GPS' test command **2000001F**.
- Check if no error is shown at the monitor.
- Use the TSW 'Free Bus Access' command 00000040 to download the almanac from the reference receiver.
- Use the TSW 'GPS' test command **2000001E**.
- Check if no error is shown at the monitor and that the coordinates are shown properly.
- Use the TSW 'Stop' command 00000006.

#### 10. Fan test

- Connect U<sub>bat</sub> to pin 1000-3 (KL-R).
- Use the TSW 'Fan' test command 3000004A.
- Check if no error is shown at the monitor.

On the following pages figure i and ii give an overview of all required connections to execute the tests.

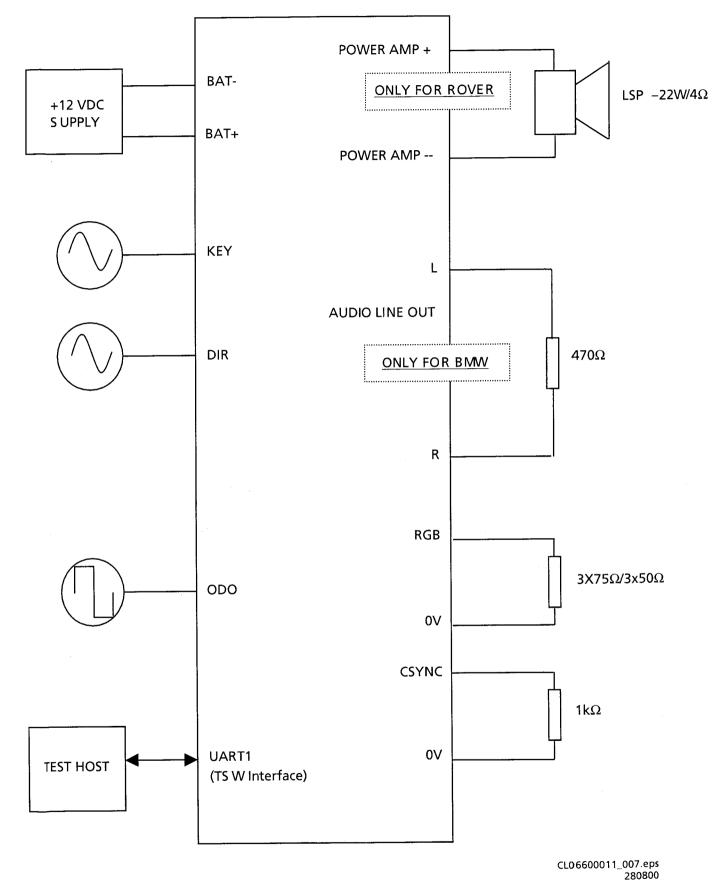
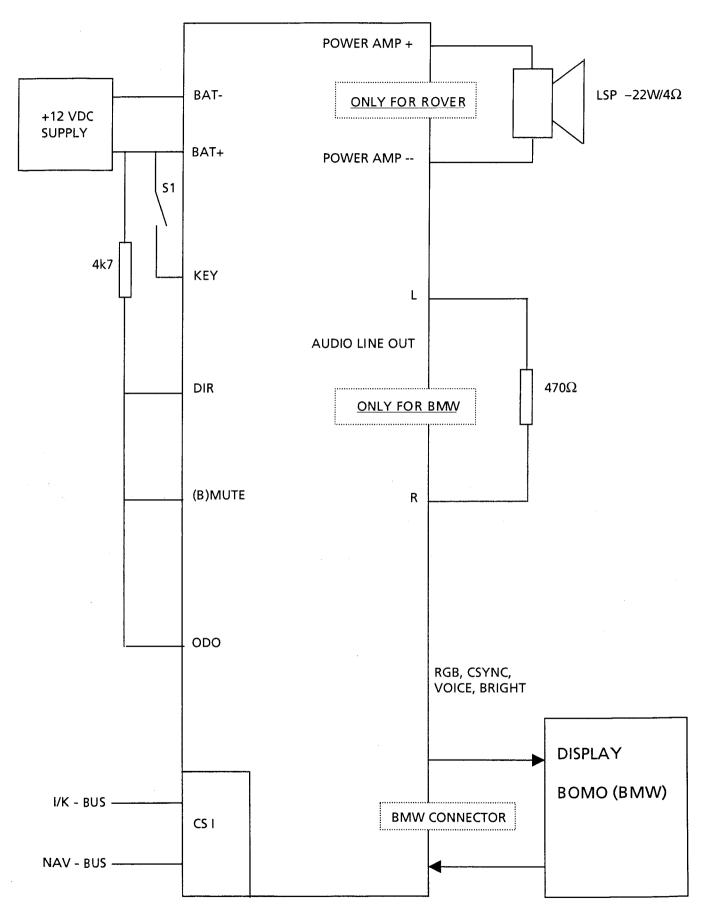


Figure i



CL06600011\_009.eps 280800

Figure ii

## **Disassembly / Re-Assembly Procedures**

#### 1. Important:

Use ESD protection equipment!

For re-assembling, follow the procedures in the reverse order and read <u>thoroughly</u> all added *notes*.

Take care that wires, cams etc. are in the right position again after re-assembling.

For the exact position of the parts, refer to the exploded view.

#### 2. CDM-M2 module pos.1002

- Remove the top cover pos.10.
- Remove 4 screws pos.9 (see figure iii).
- Lift the module.
- Loosen the 14-pole flat cable from the main pcb connector.
- Take the module out.

#### Notes:

- When re-assembling, take care that the dust cover is inserted and that it resides between the module and the right rear screw, i.e. NOT between screw and fan! See figure iv.
- Fix the screws in the order as shown in figure iii.

#### 3. Gyro assy pos.1004 (only -/235 version)

- Remove the CDM-M2 module pos.1002 (see '2.').
- Remove the dust cover pos.8.
- Loosen the gyro flat cable from the main pcb connector.
- Remove the two screws pos.5.
- Take out the gyro assy.

#### 4. Front assy pos.1001

- Remove the CDM-M2 module pos.1002 (see '**2**.').
- Remove the dust cover pos.8.
- Remove the two screws pos.18.
- Remove the bottom cover pos.11.
- Lift the four brackets of the front at the four corners in such a way that the brackets become free from the chassis.
- Remove the flexfoil from the main pcb connector.
- Take off the front assy.

#### 5. GPS receiver assy pos.1003

- Remove the CDM-M2 module pos.1002 (see '2.').
- Remove the dust cover pos.8.
- Remove the flat cable pos.4 from the connectors on the receiver and main pcb.
- Remove the hex fixation nut and ring from the antenna connector.

- Slide the receiver forward so that the two receiver front holders becomes free from the chassis.
- Take the receiver out upward.

#### Notes:

- When re-assembling, take care that the two receiver front holders grasp into the fork-shaped spacings of the chassis.
- When re-inserting the flat cable, take care that the red marked side is at the top position of the receiver connector and at the front position of the main pcb connector!

  Refer to figure v.

#### 6. Fan assy pos.2

- Remove the CDM-M2 module pos.1002 (see '2.').
- Remove the dust cover pos.8.
- Disconnect the fan cable from the main pcb connector.
- Cut the four fan strings pos.3 just between the rounded part and the chassis (at the rear) and take out the fan.

#### Notes:

- When re-assembling the fan, follow this procedure:
- Insert the conical end of the strings at the <u>label</u> side of the fan.
- Pull each string until (after two slight clicks) the big rounded end just rests in the hole of the fan.
- Hold the fan with the cable in <u>top</u> <u>right</u> position.
- Put four <u>new</u> strings through the four holes at the chassis rear <u>simultaneously</u>.
- Pull <u>each</u> of the strings through the holes until one of the smaller rounded parts just goes through the holes (after one slight click).
- Look if the fan is mounted properly and reconnect the fan cable to the main pcb connector.
- Cut off the four conical parts of the strings just behind the rounded parts.

#### 7. Main pcb assy pos.1000

- Remove the CDM-M2 module pos.1002 (see '2.').
- Remove the dust cover pos.8.
- Remove the front assy pos.1001 (see '4.').
- Disconnect the following cables from the main pcb:
  - GPS receiver cable pos.4.
  - Fan assy cable pos.2.

- Unscrew the following screws:
  - Main connector bracket fixation screw pos.16.
  - Two gyro bracket fixation screws.
  - Four main pcb fixation screws pos.6.
  - Take out the main pcb.

#### Notes:

- When re-assembling the main pcb, take care of the following:
- The EMC protection covers pos.25 must be fitted thightly and properly at their places (no bent lips).
- The main pcb must rest close to the four fixation points (no play of pcb).
- The four screws must be fixed in the order as shown in figure **vi**.
- The connector fixation screws must be tightened properly.

## 8. Figures

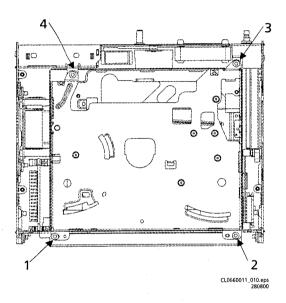


Figure iii

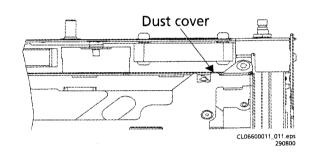


Figure iv

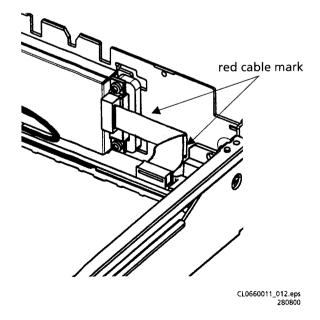


Figure v

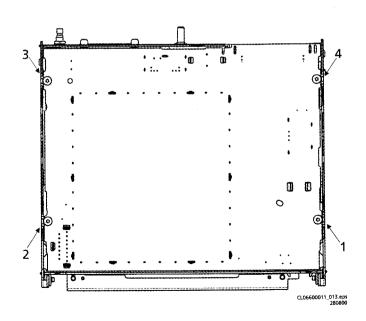
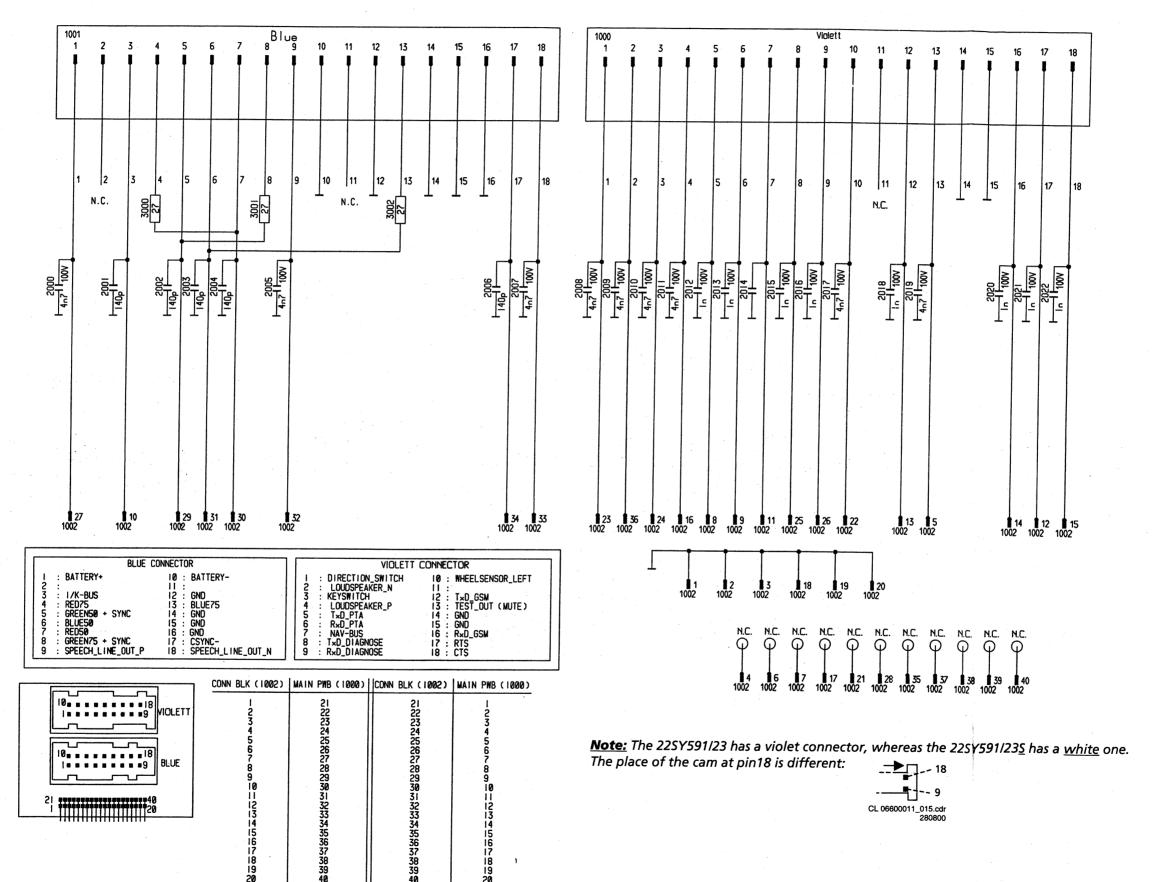


Figure vi

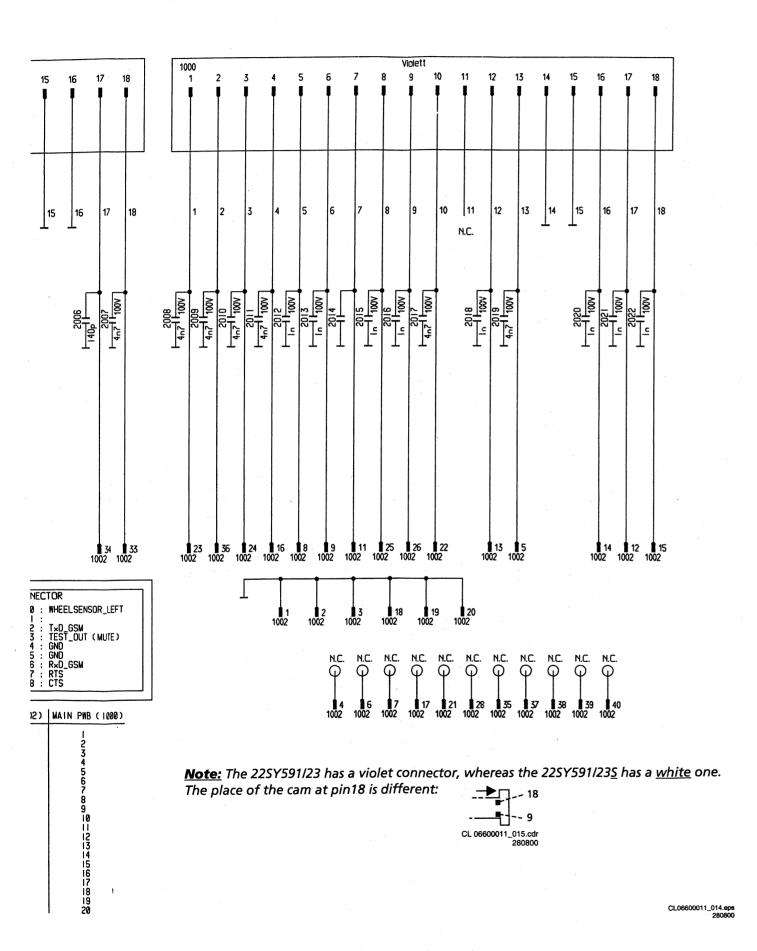
### **Connector block**

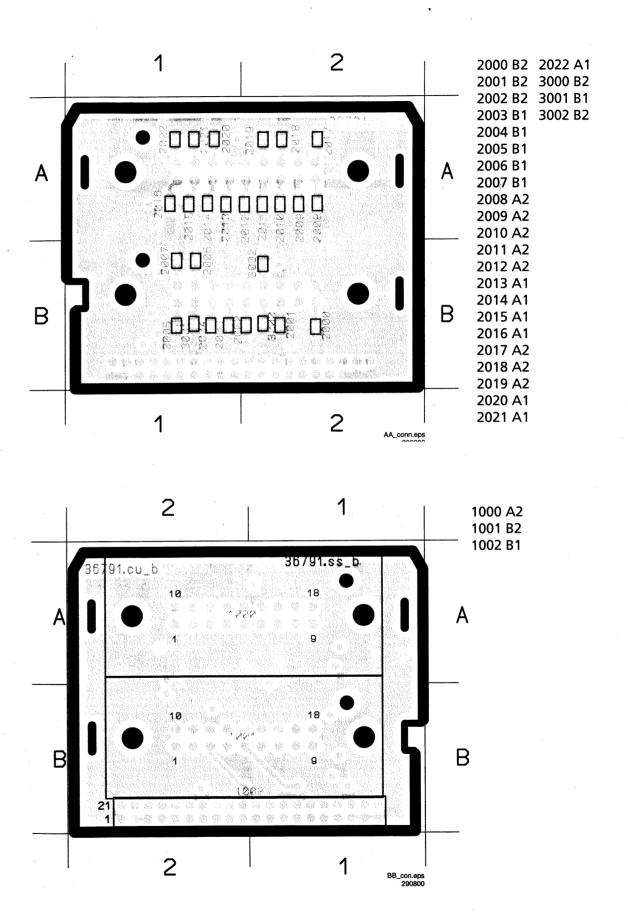


PCS 106 390

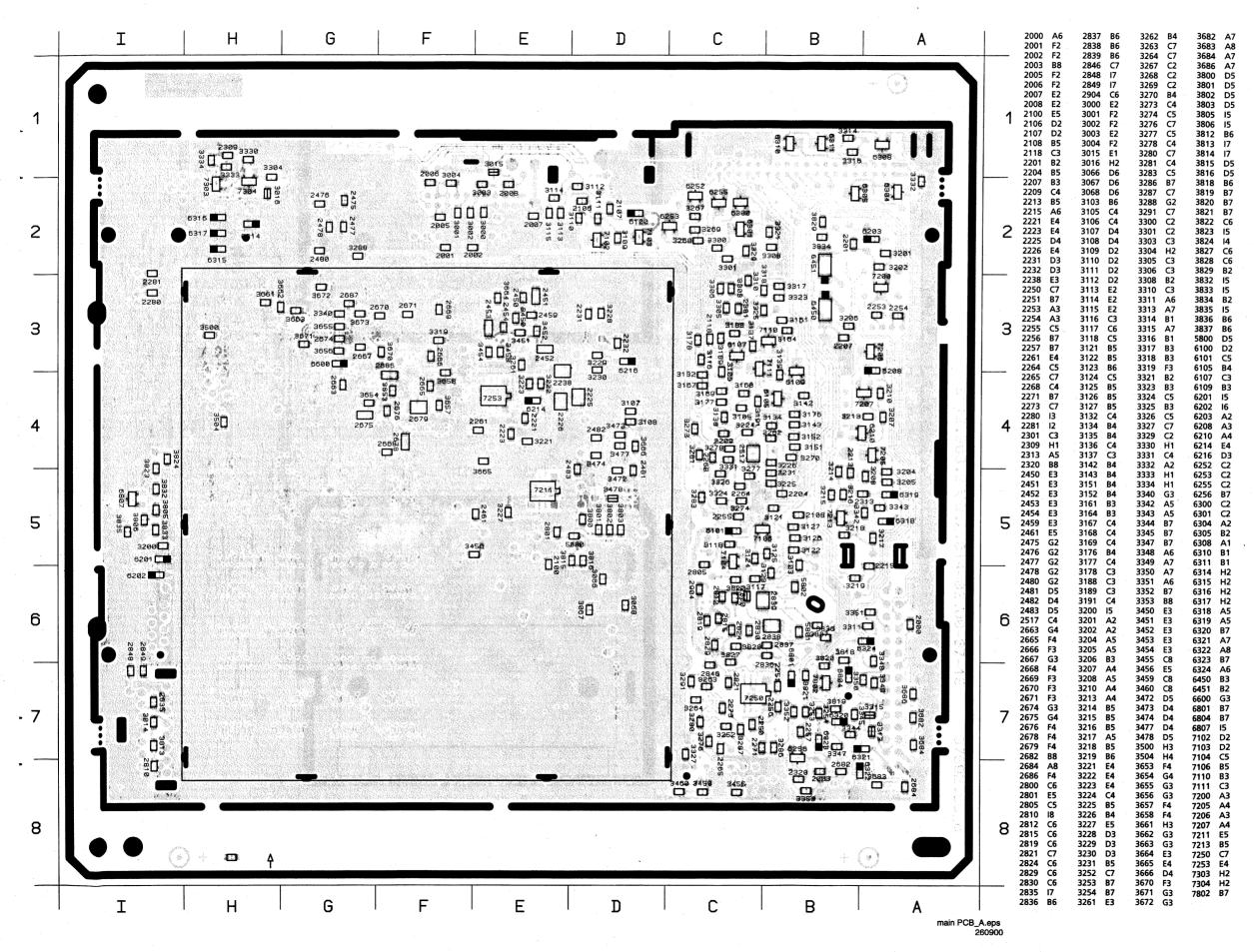
CL06600011\_014.ep:

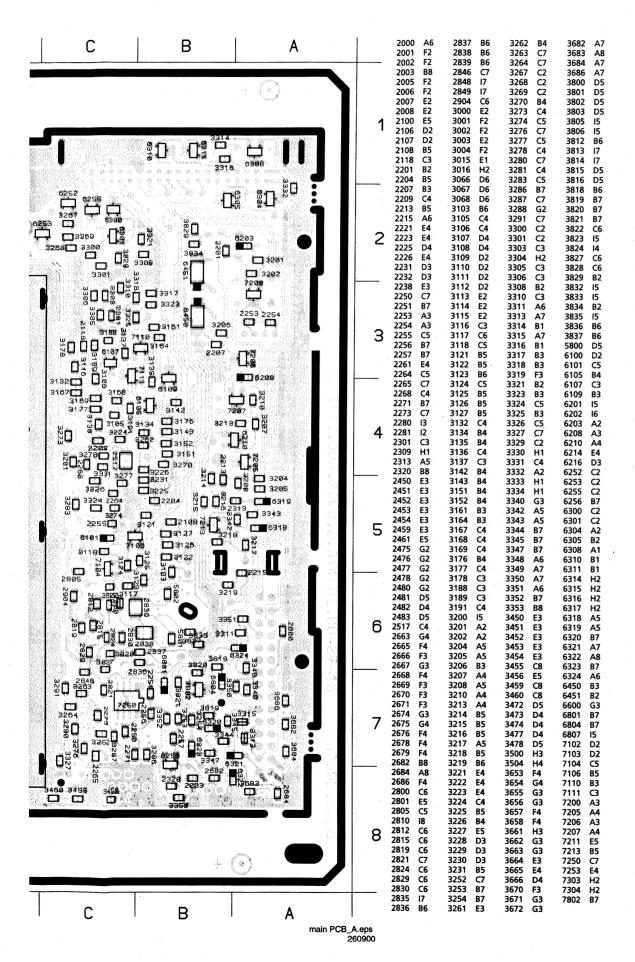
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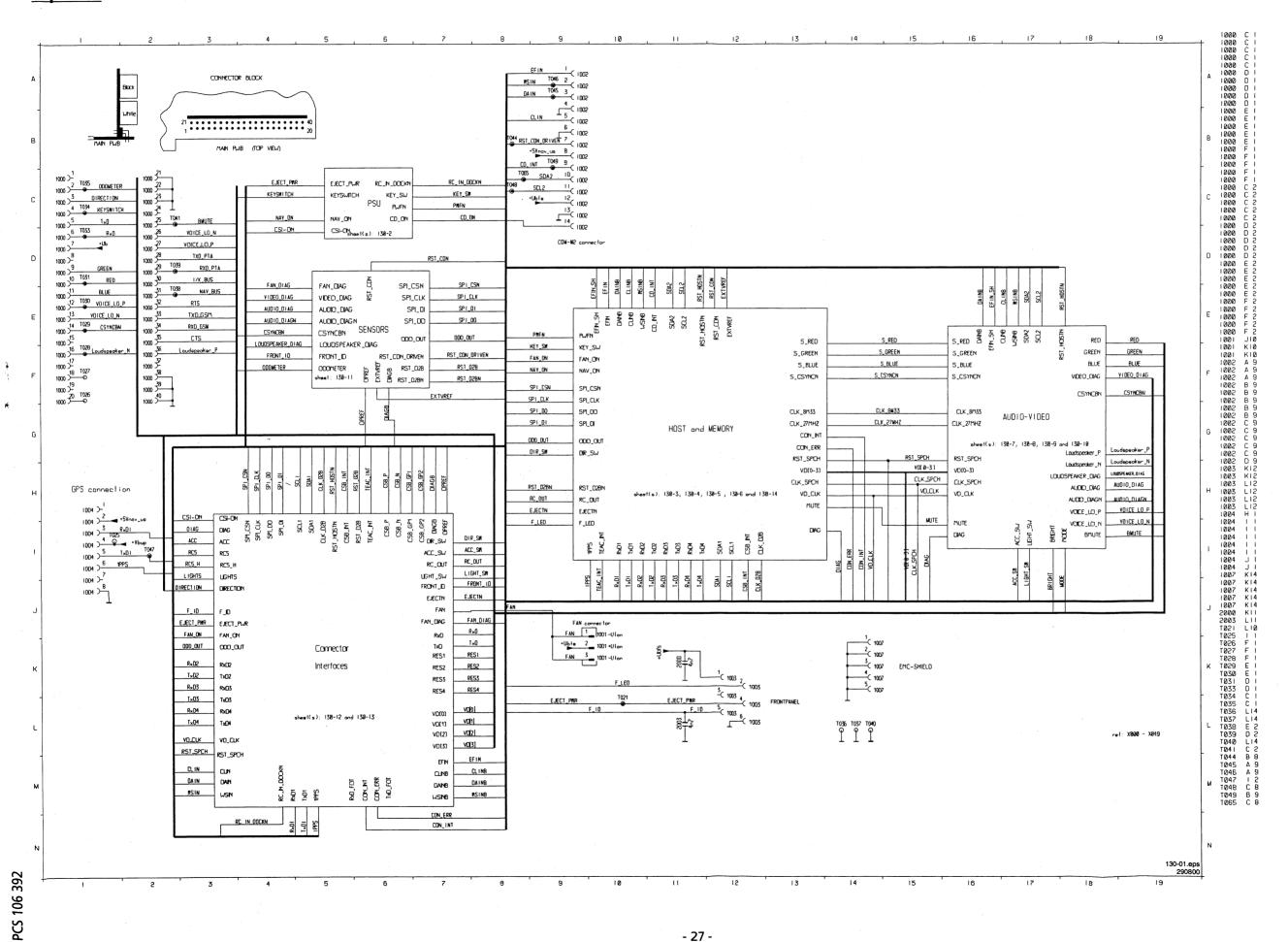


#### **Main PCB - A side**

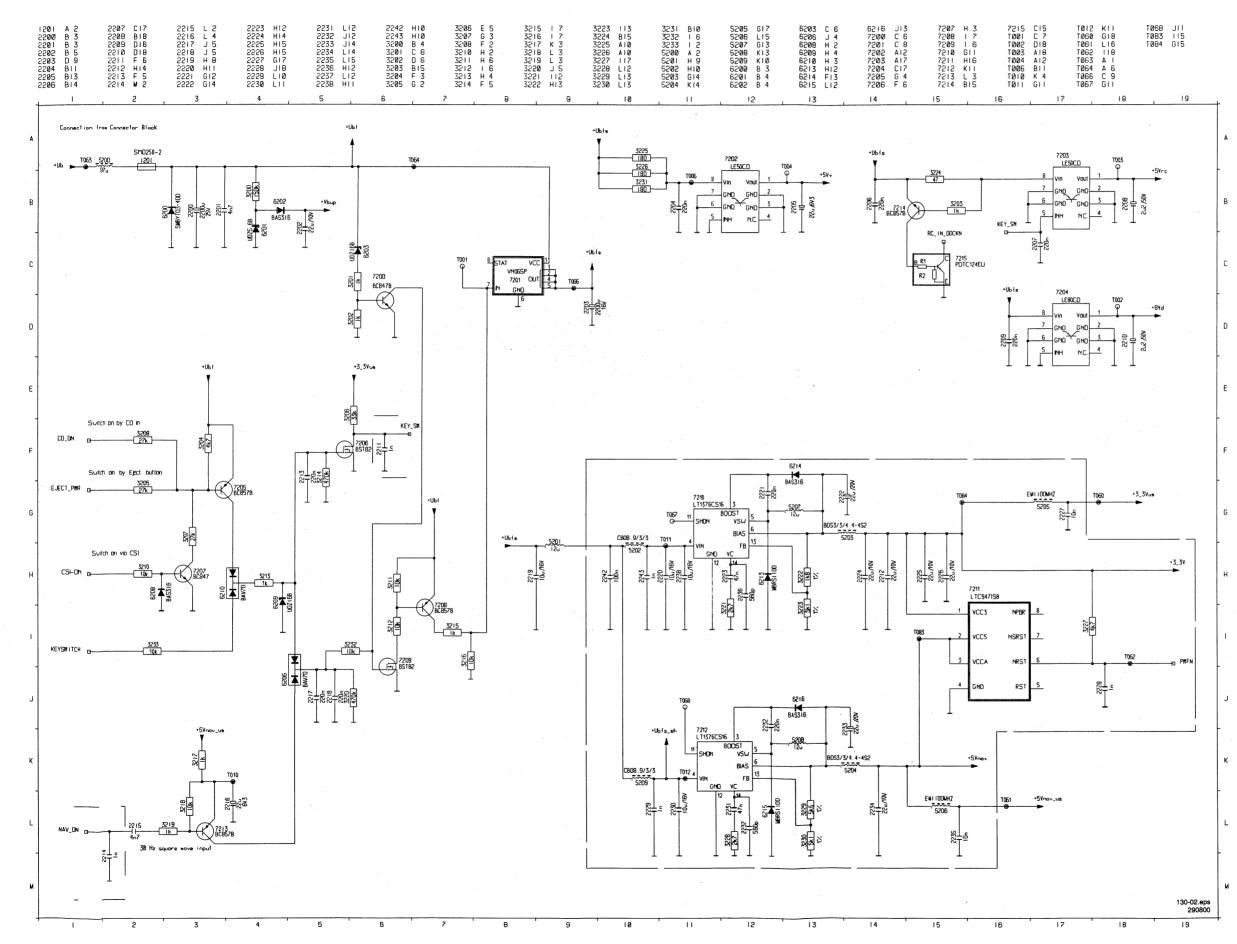


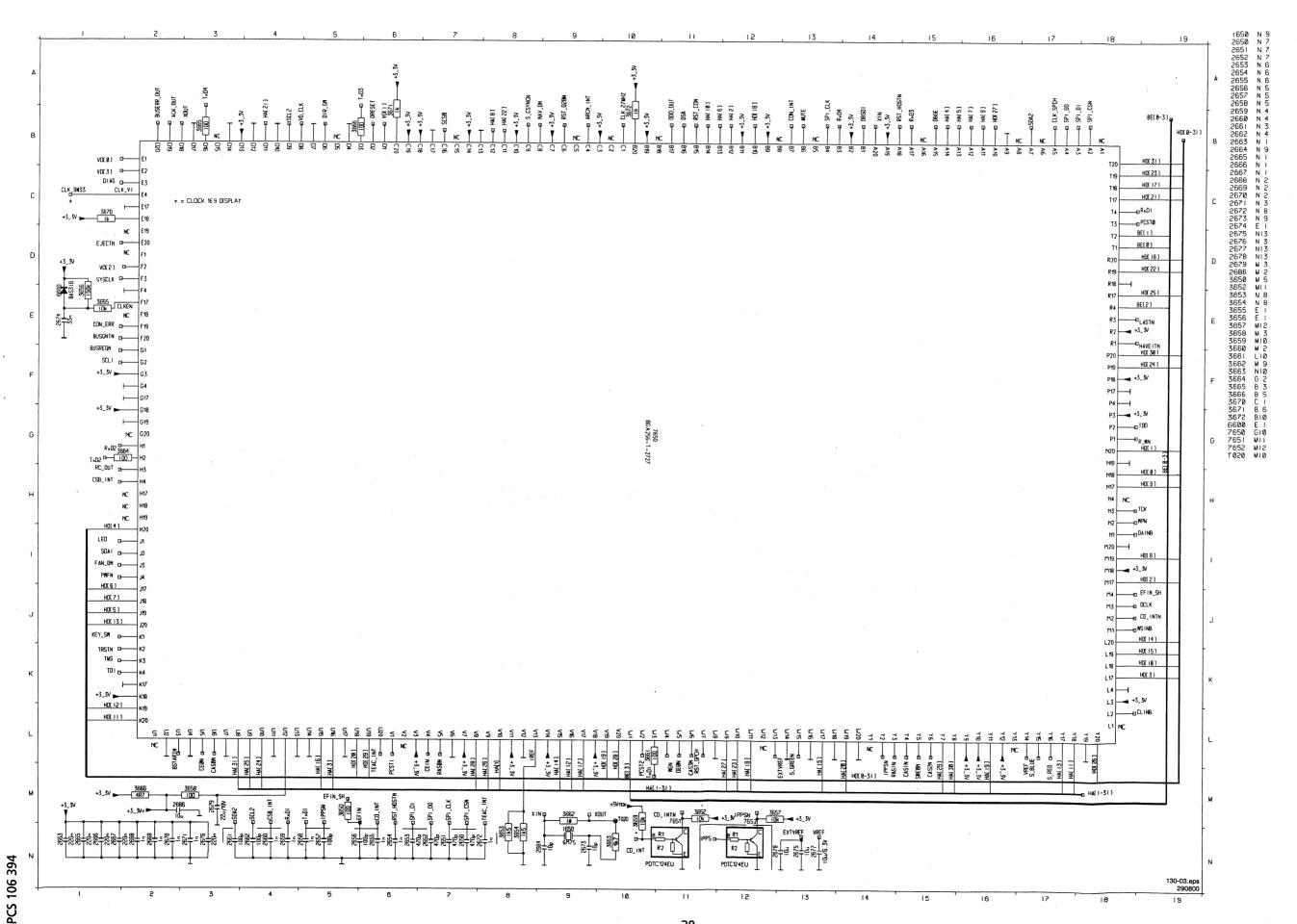


### Top level

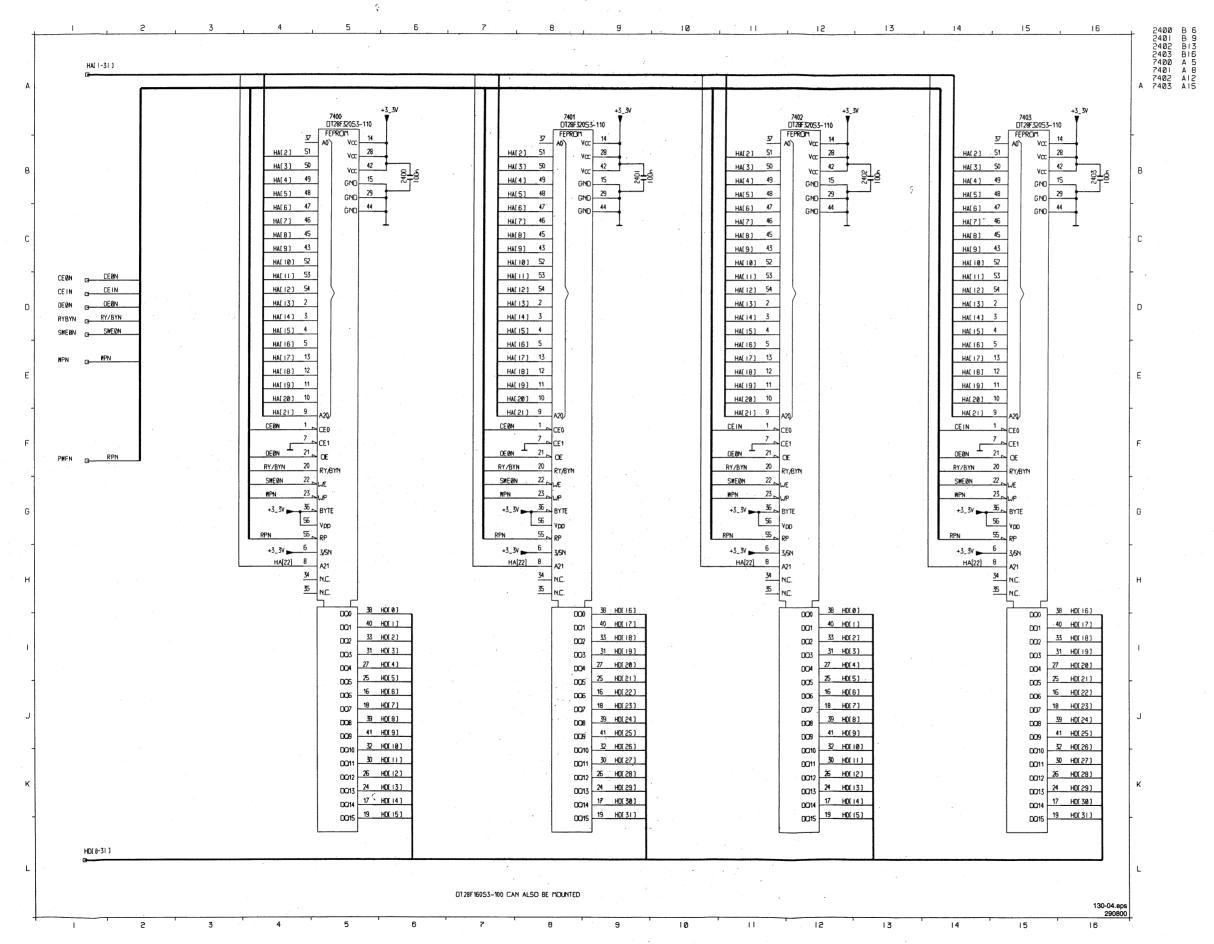


### **Power supply**



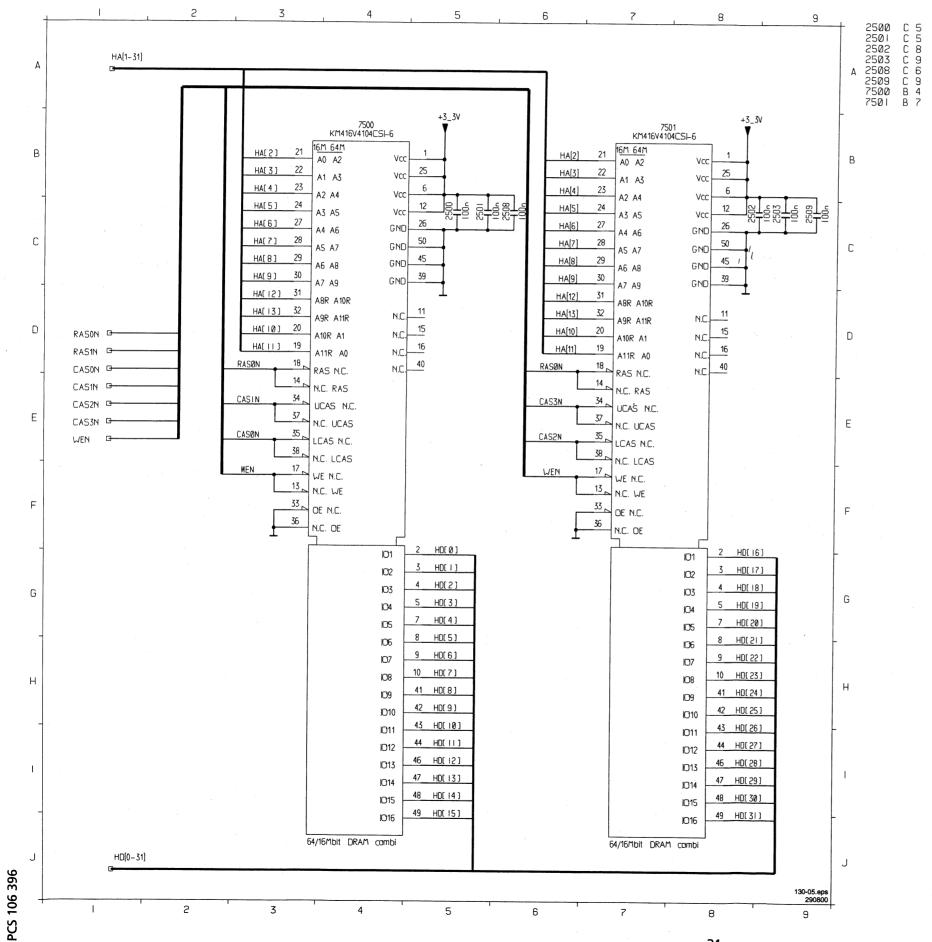


#### Flash memory

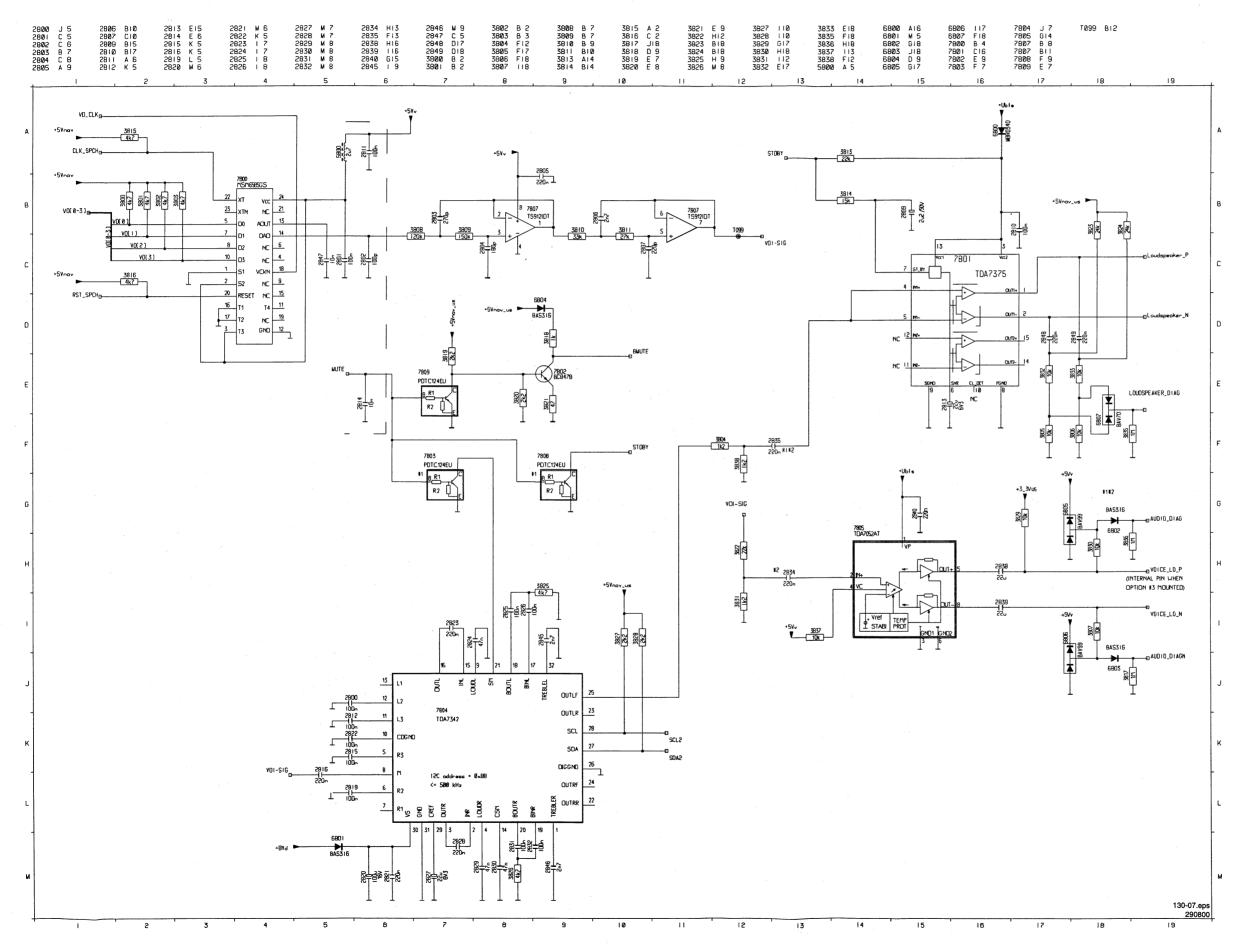


25 106 395

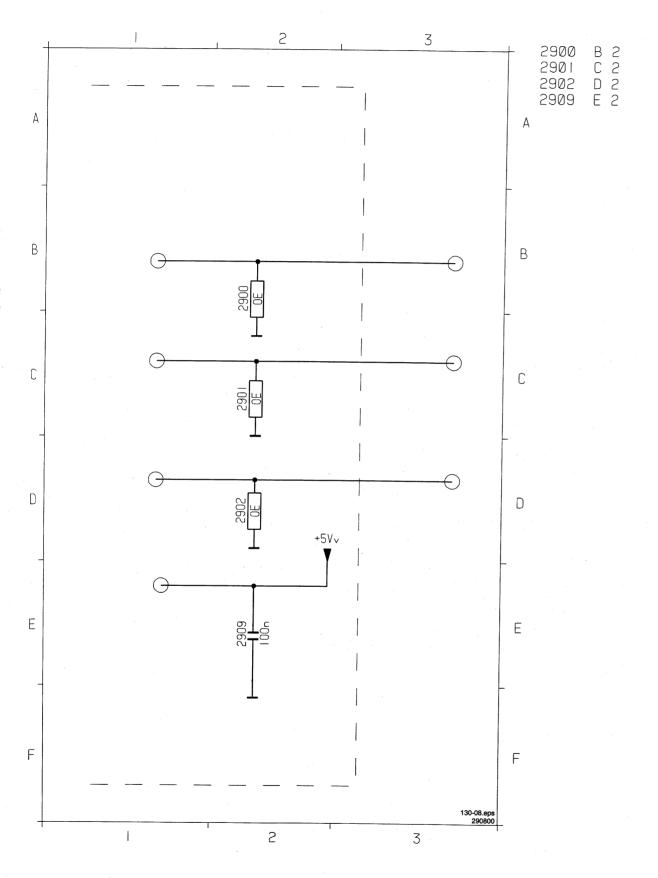
### **DRAM**



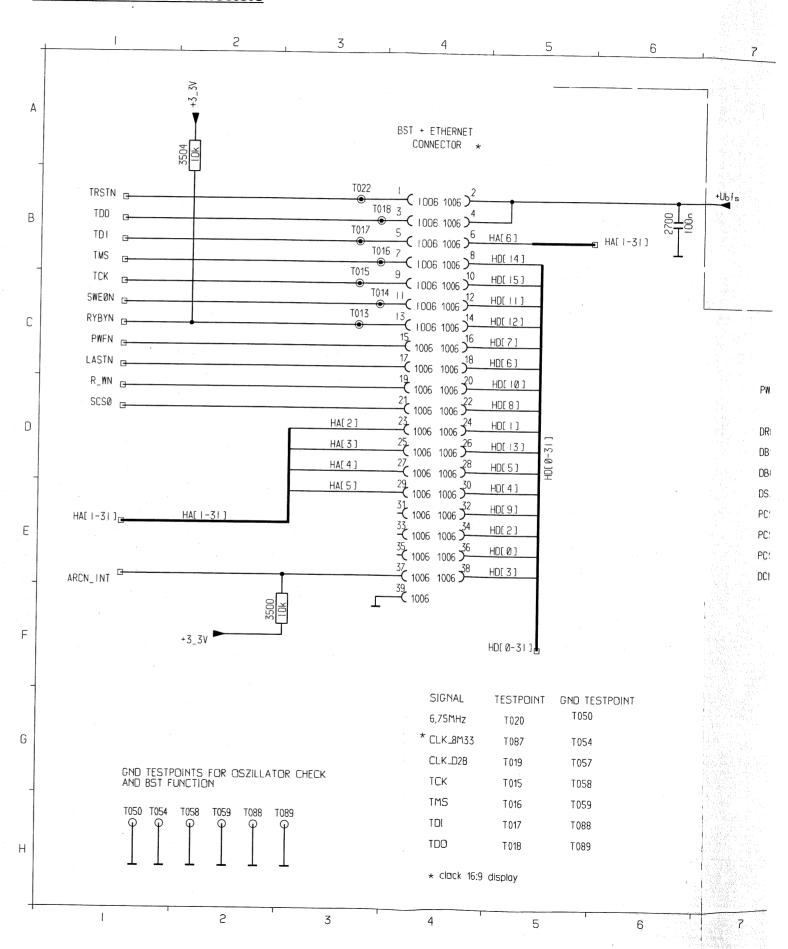
### **Speech & Line-out**



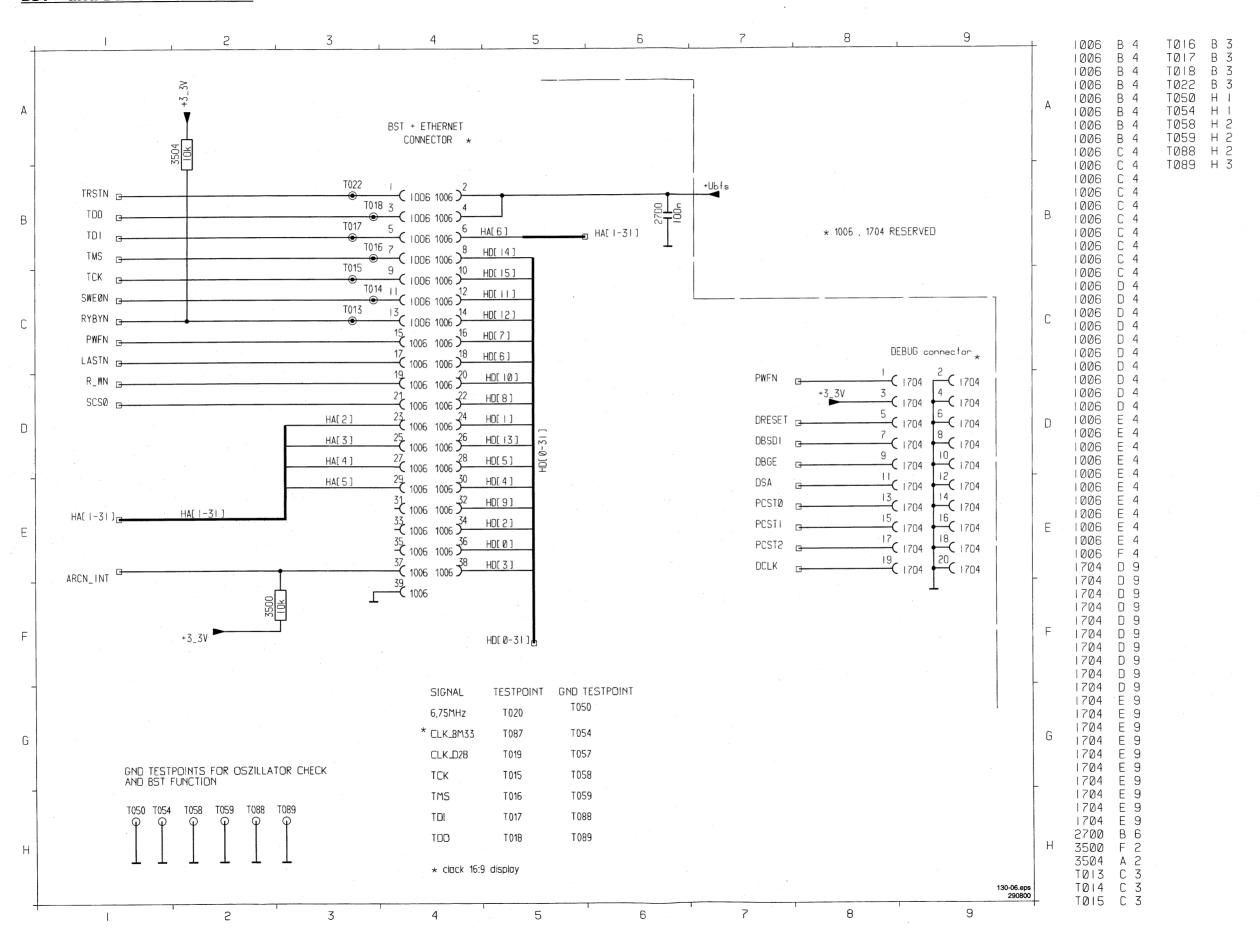
## **CD analogue output**



### **BST - and DEBUG connectors**

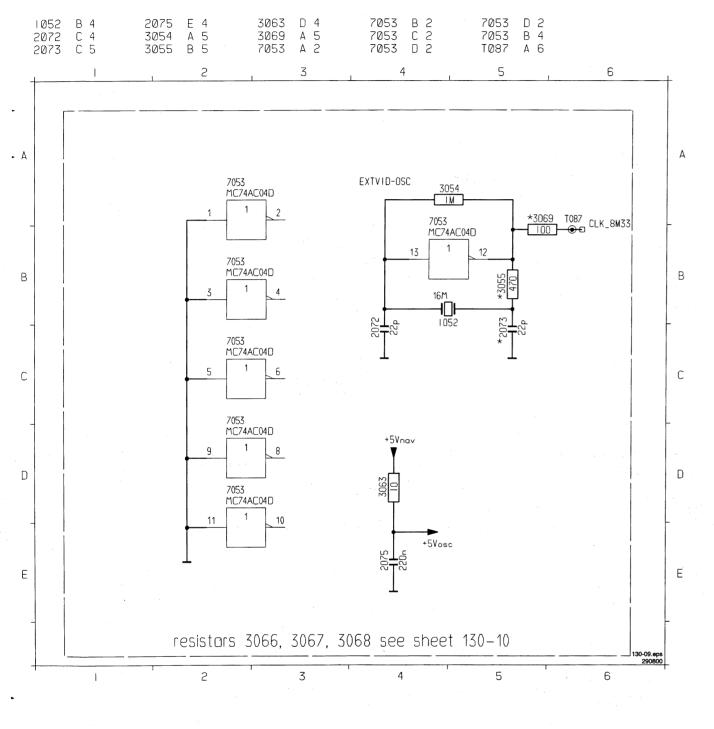


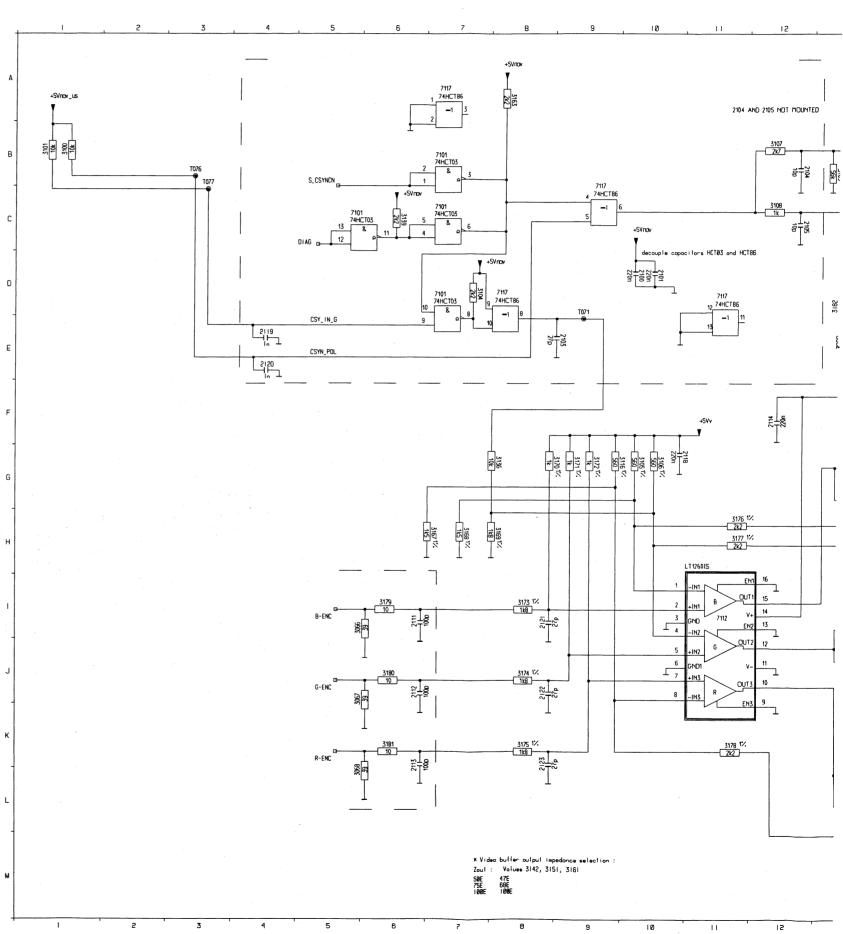
### **BST - and DEBUG connectors**

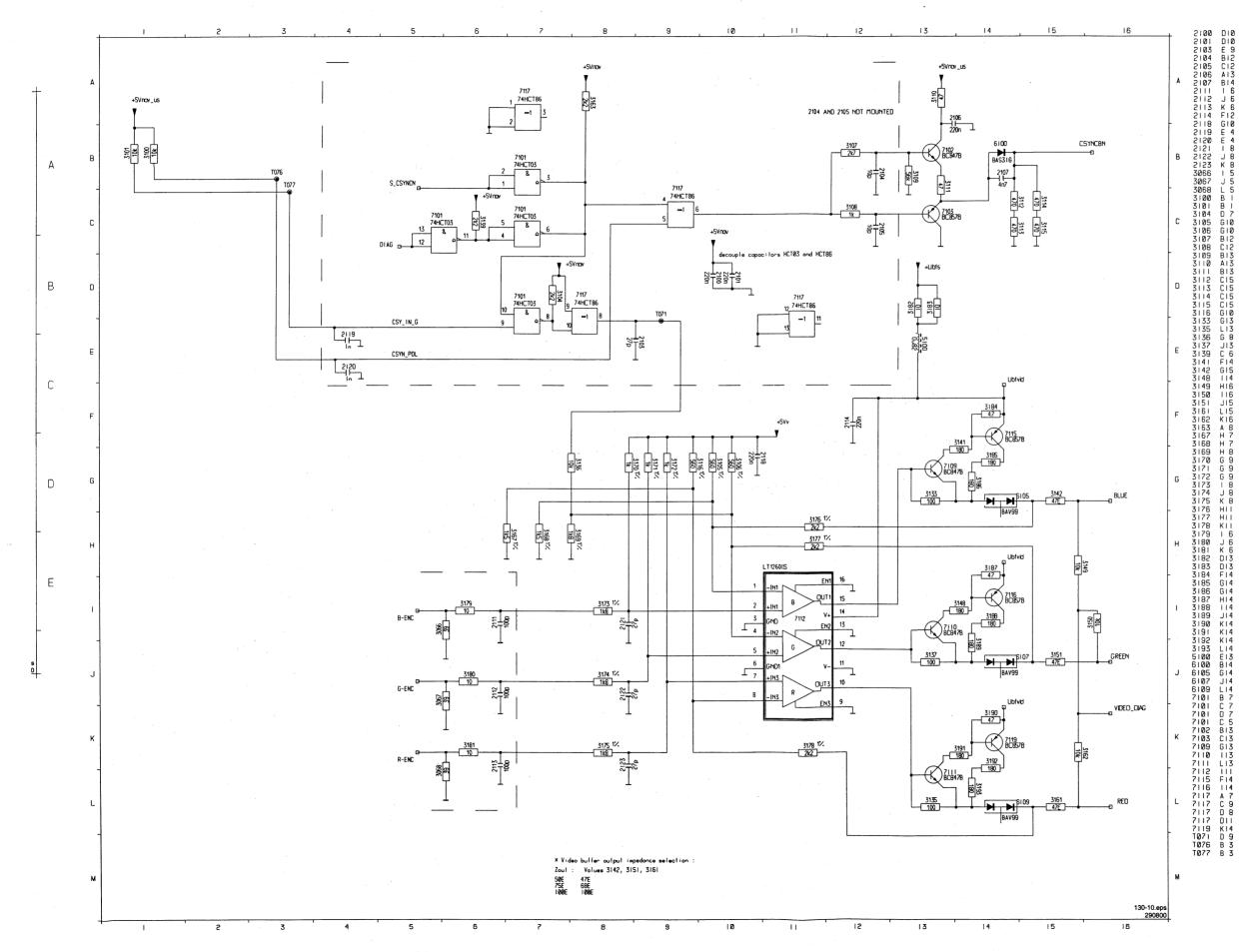


22SY591

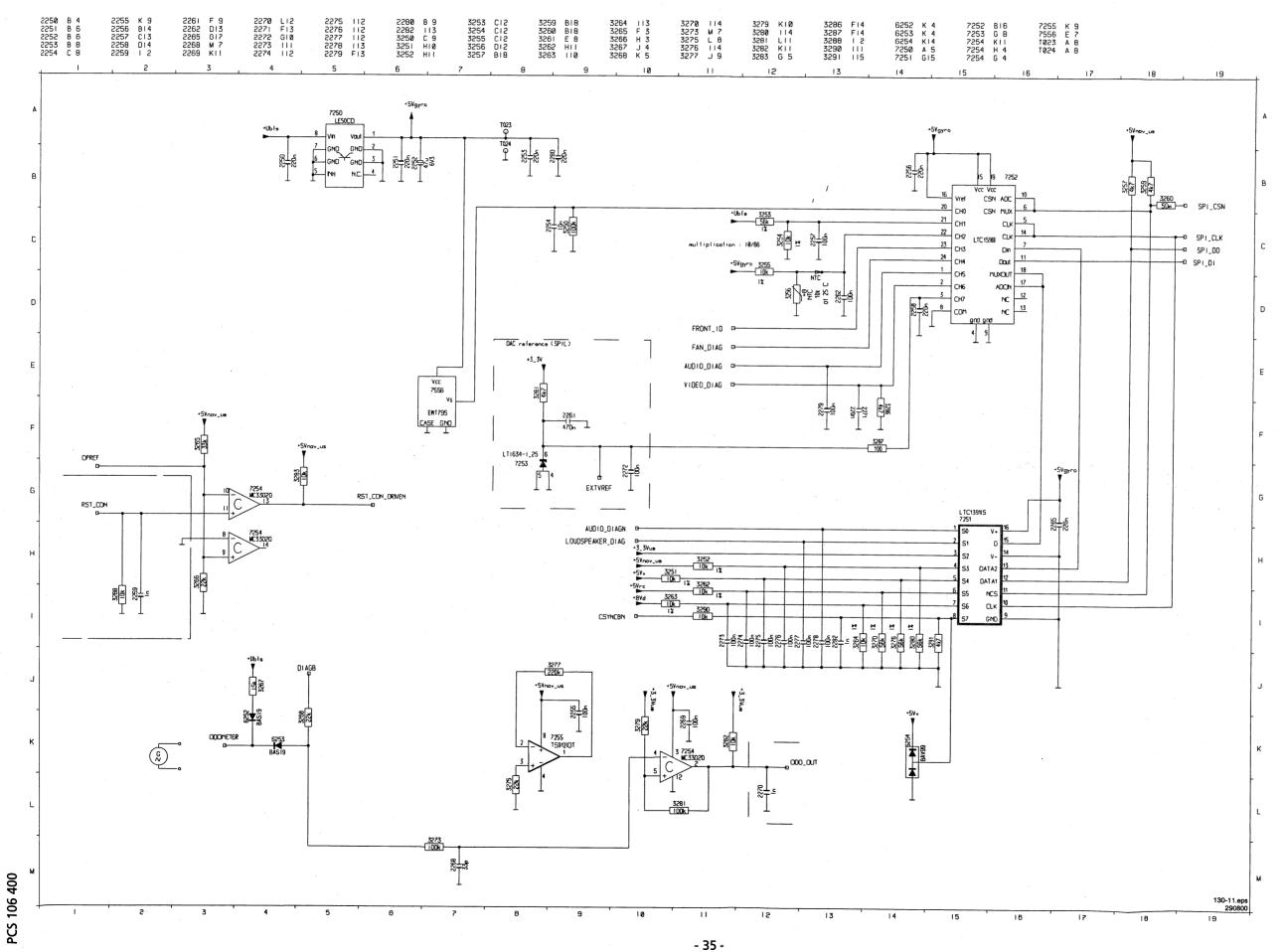
### **Video interface**



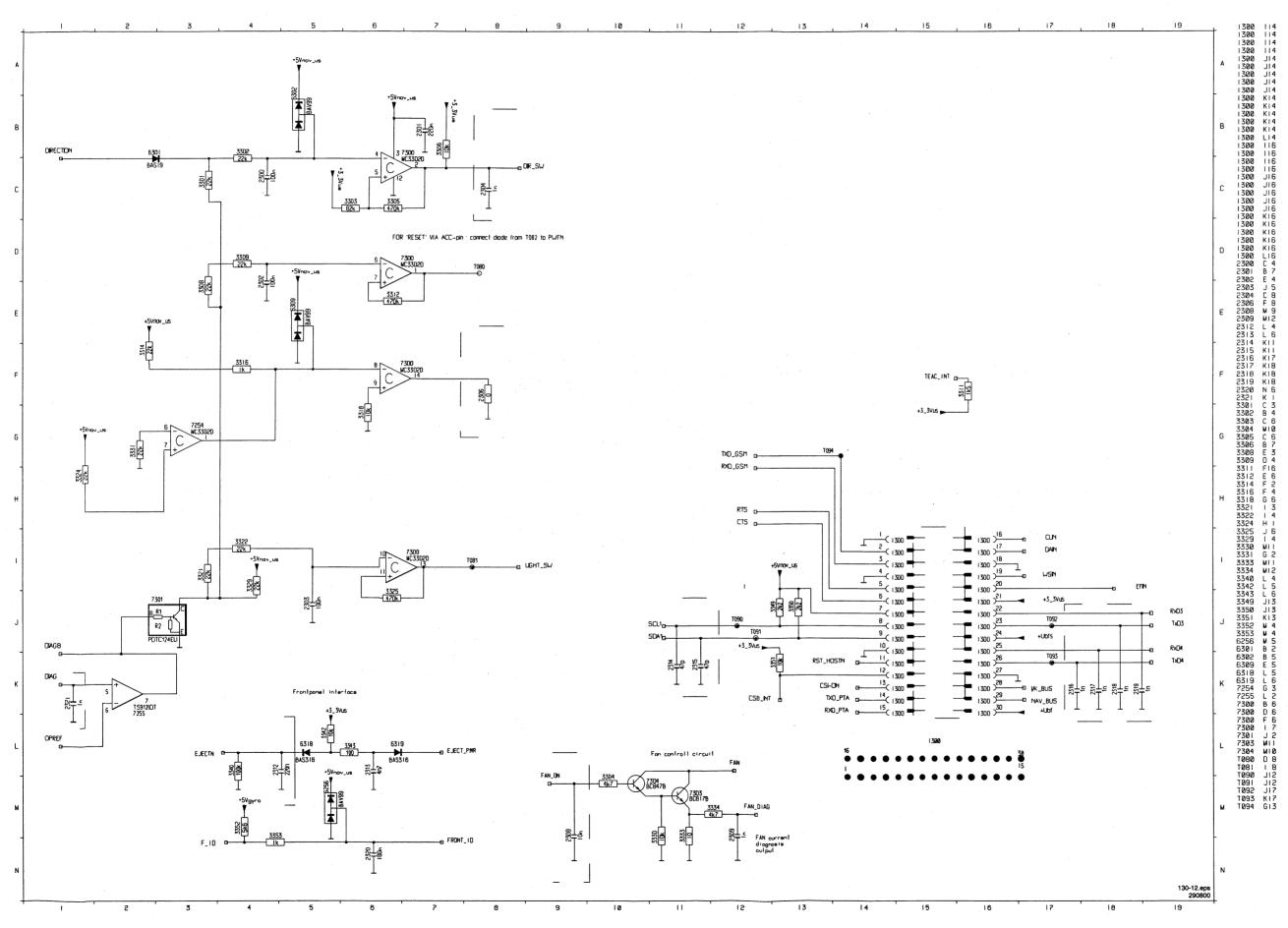




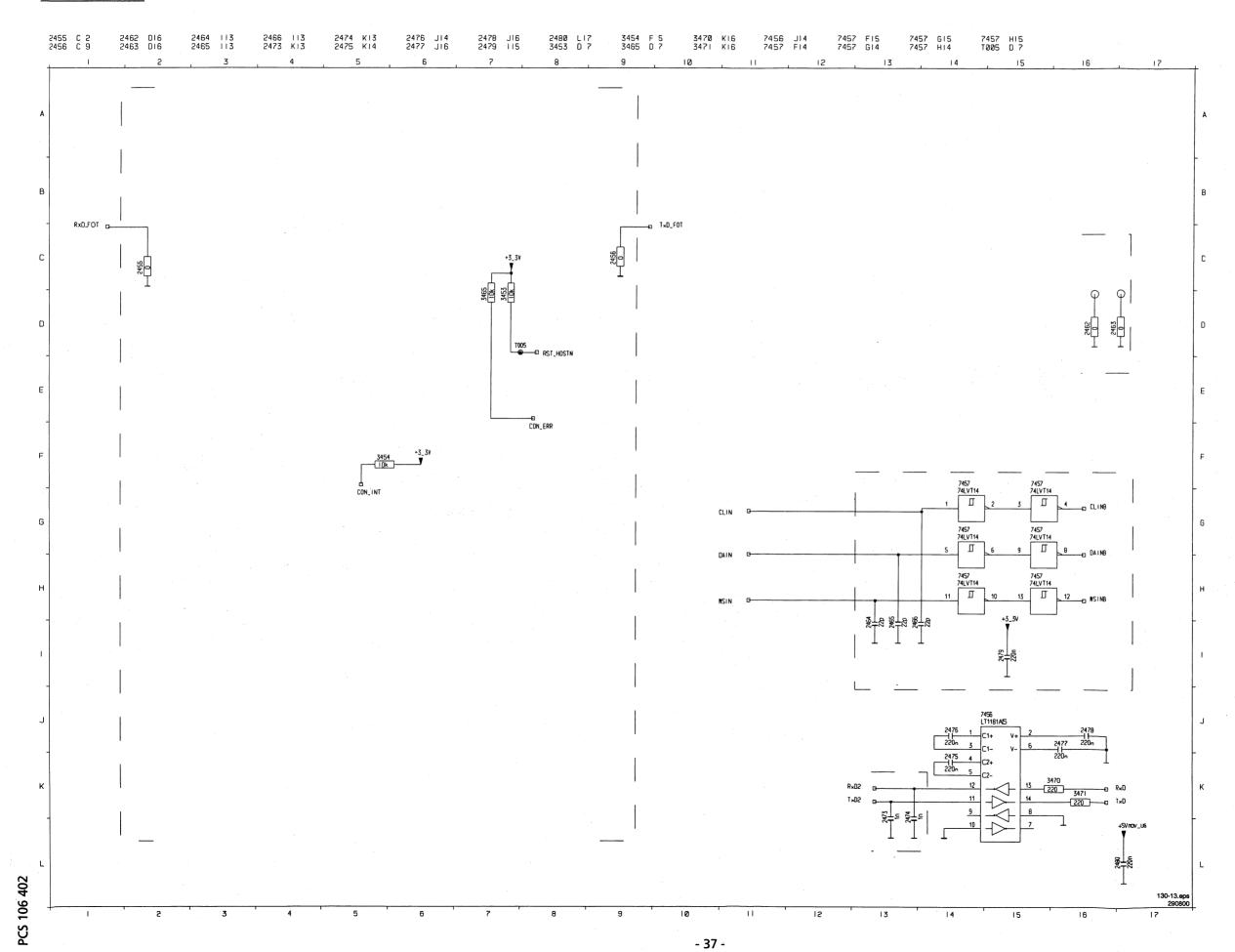
#### **Gyro -, ADC - & Odometer interface**



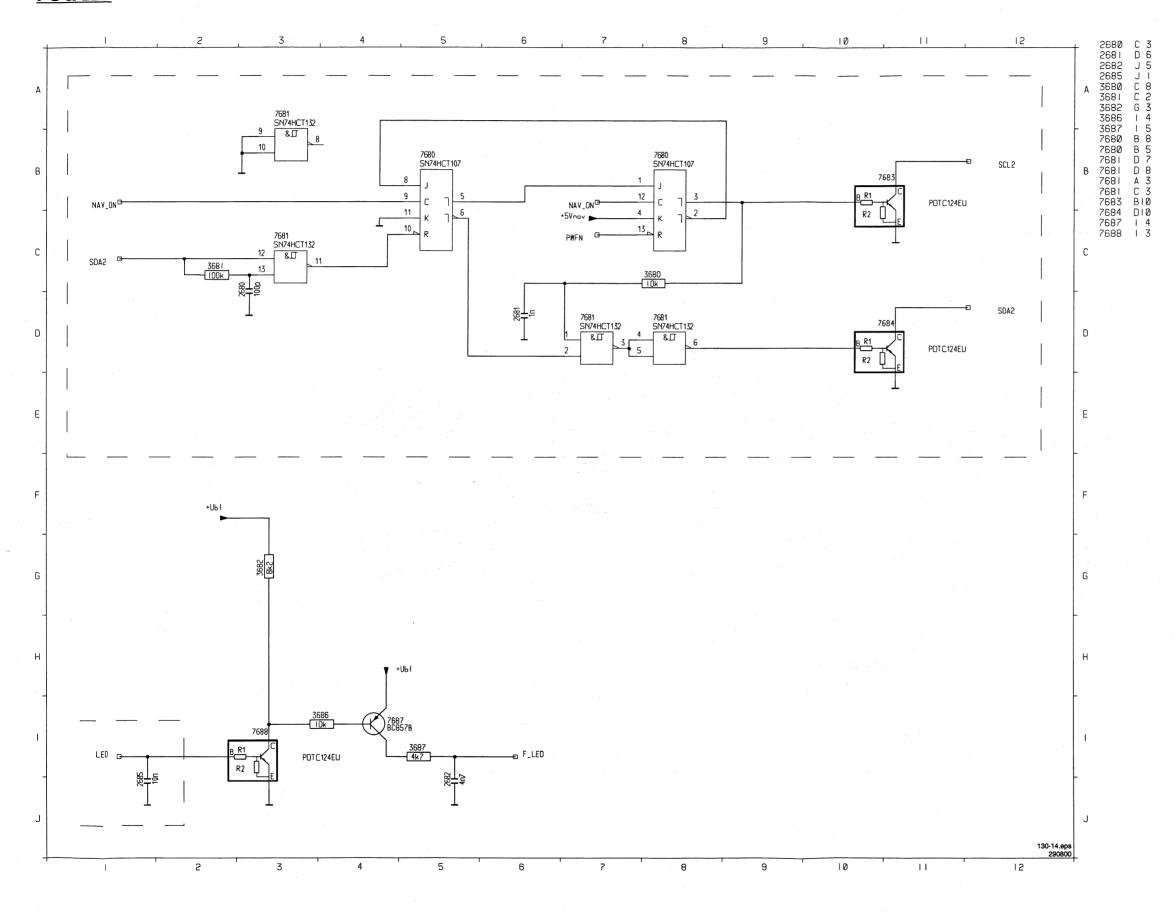
#### **Sensor & CSB interface**

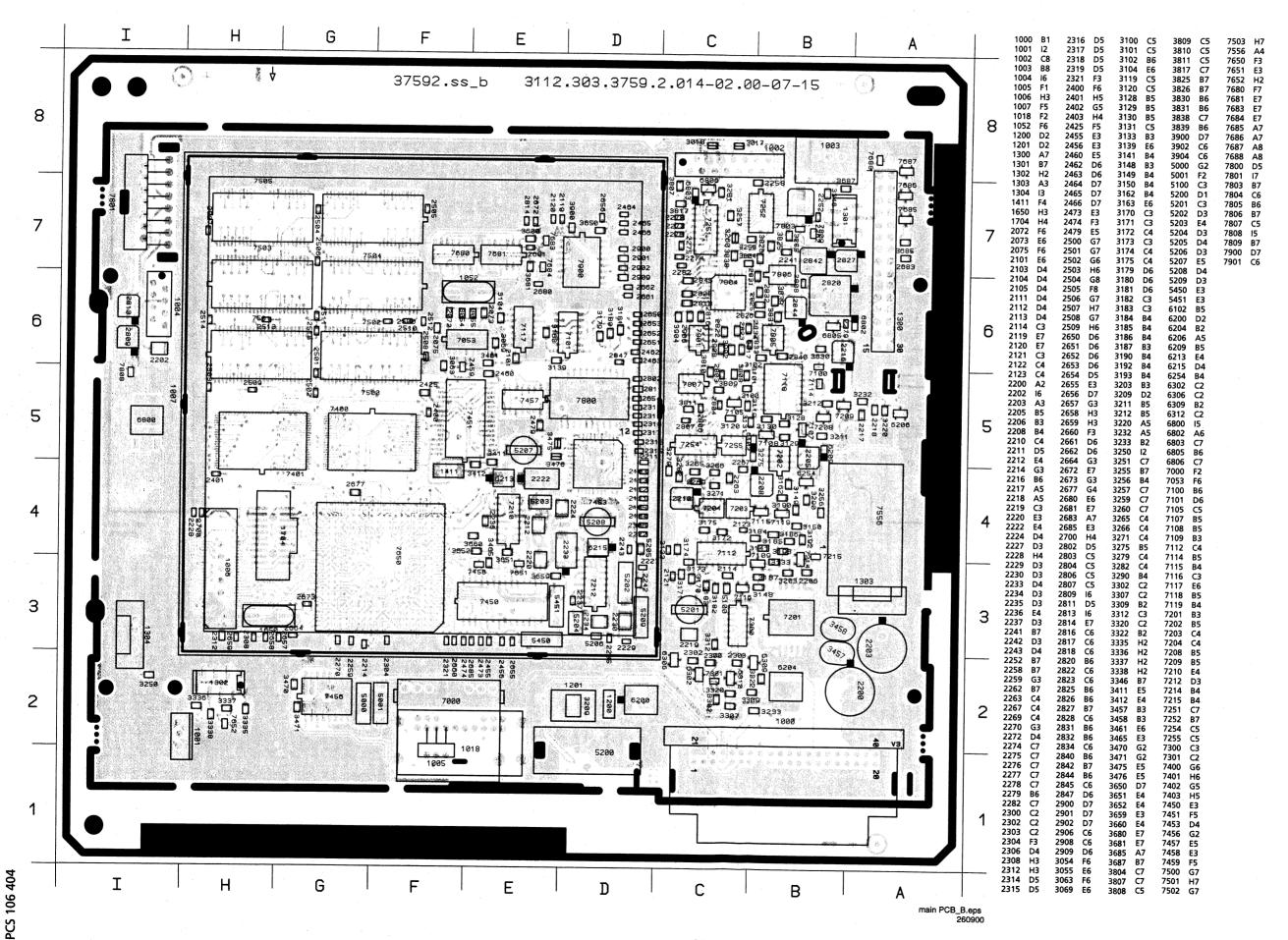


## **Bus interface**

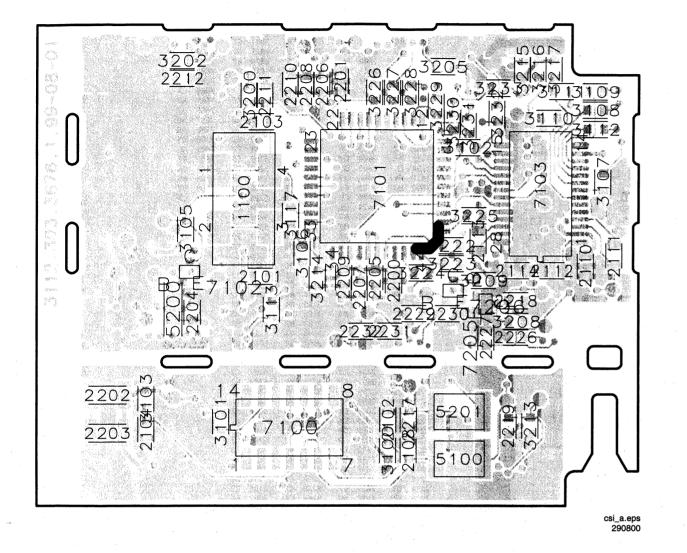


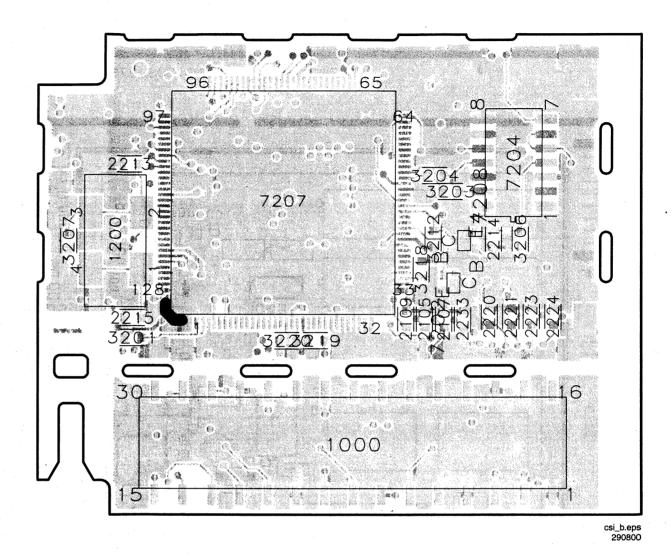
# I2C & LED





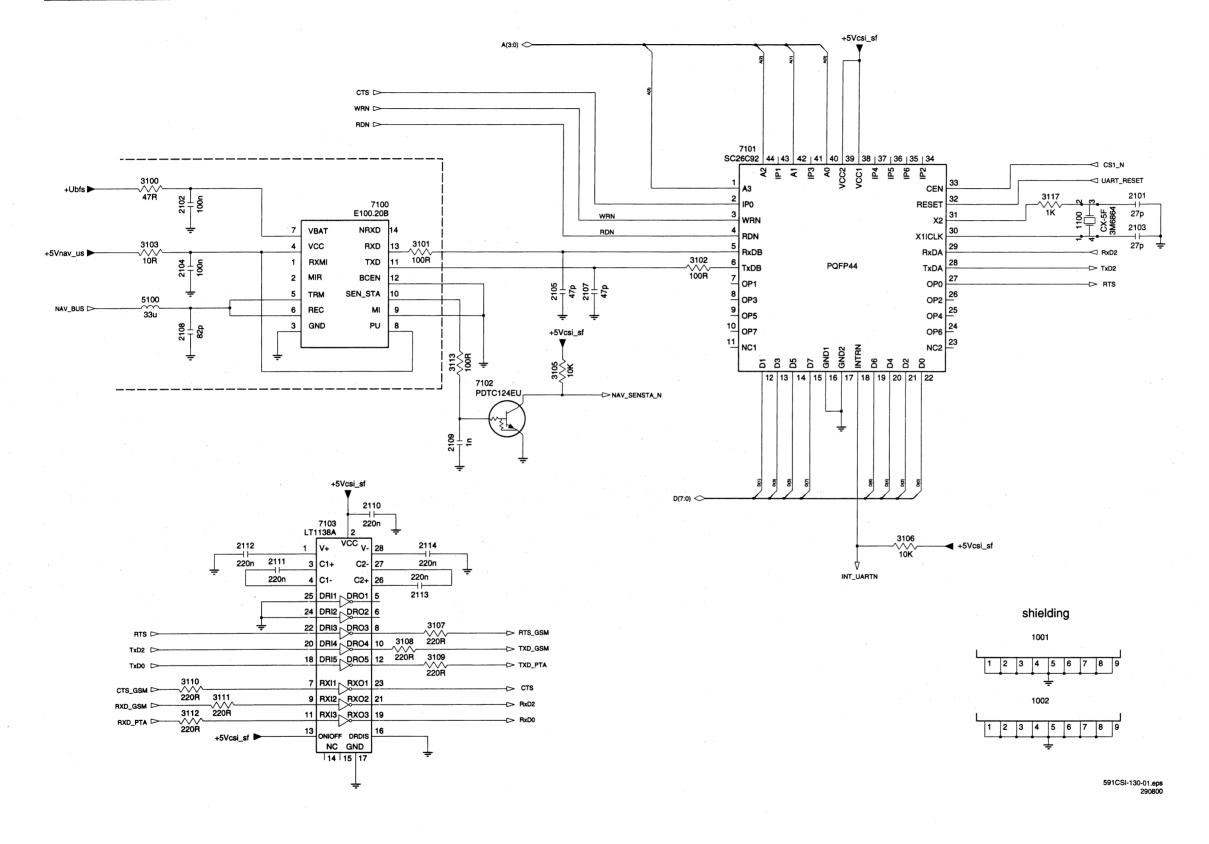
## **CSB PCB layouts**



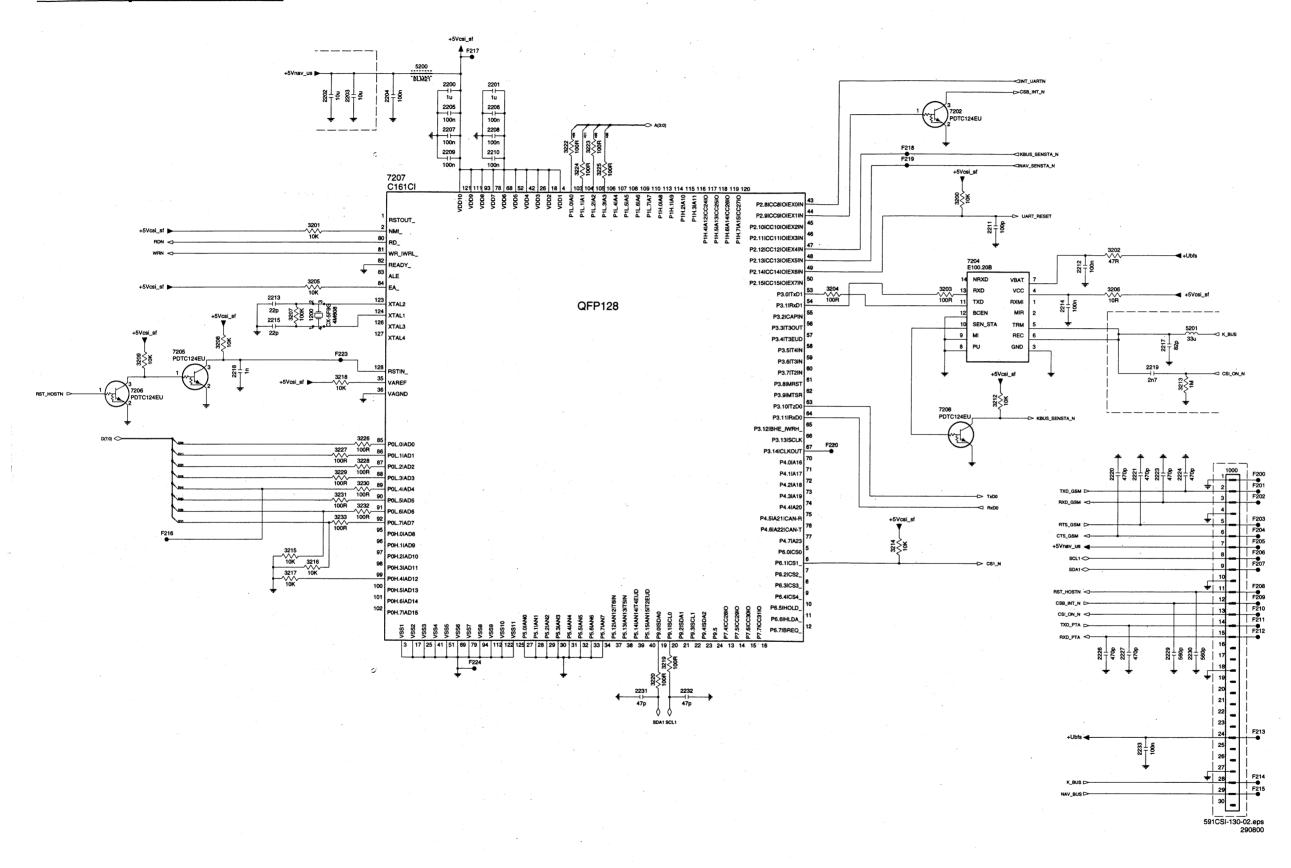


**CS 106 405** 

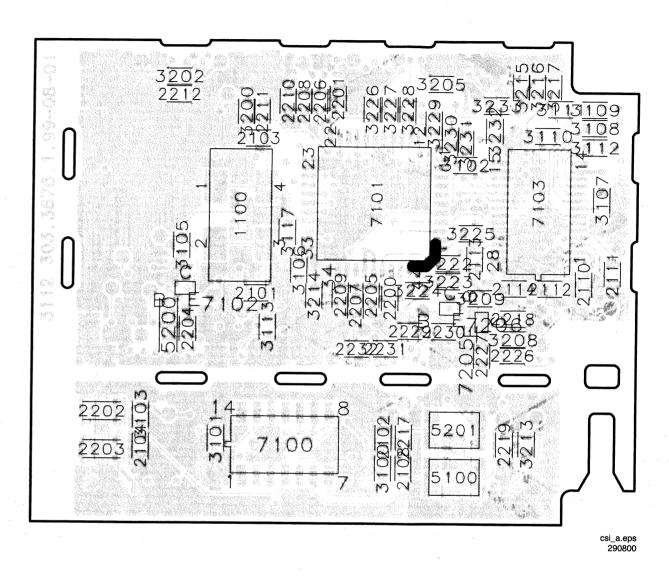
### **CSB - Interfaces**



## **CSB - Microprocessor & I/K bus**

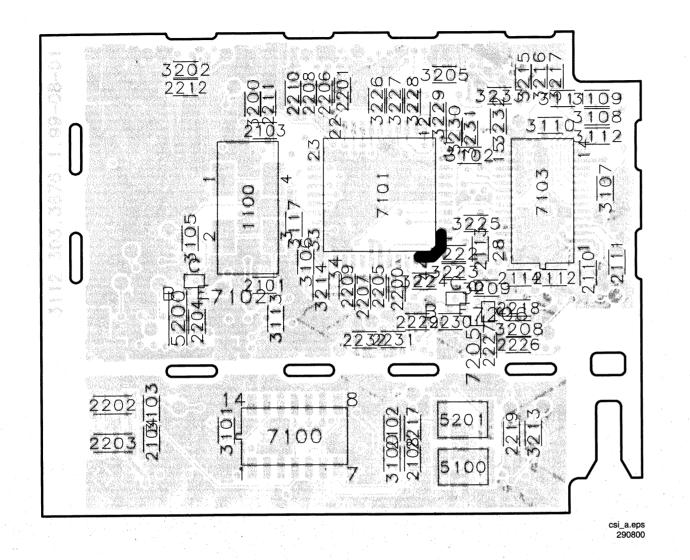


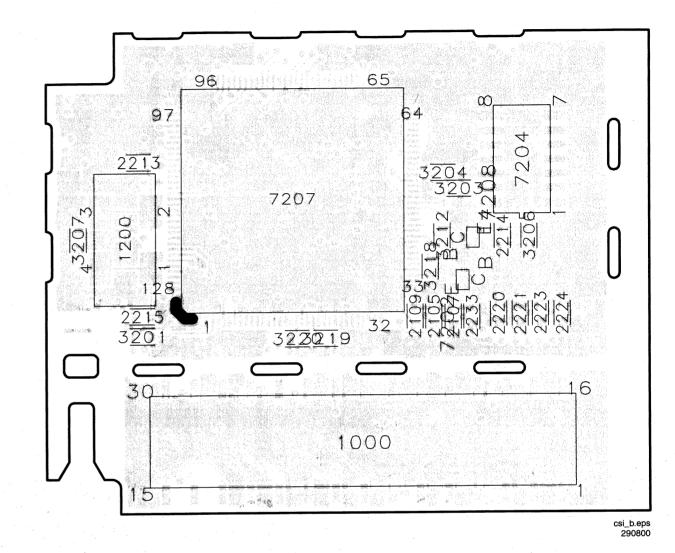
# **CSB PCB layouts**



DCC 106 409

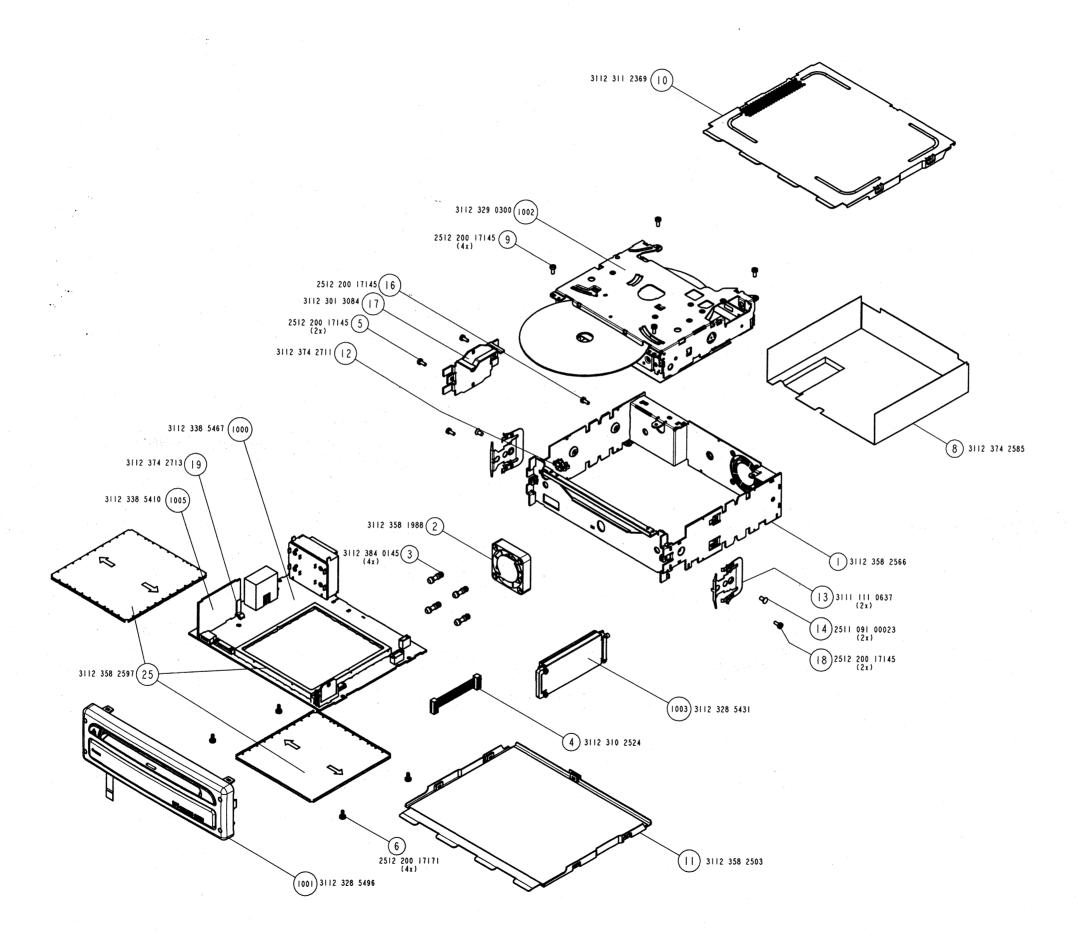
### **CSB PCB layouts**





22SY591

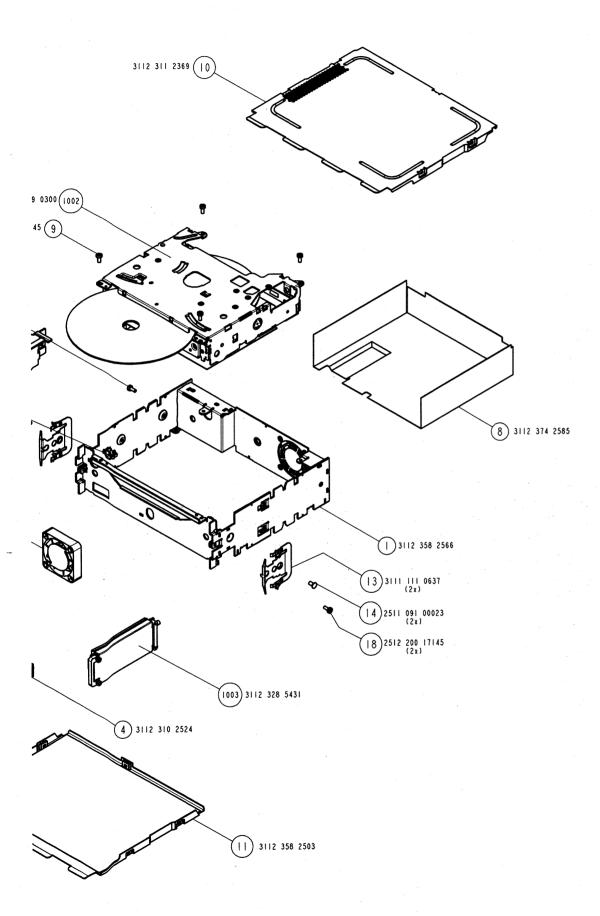
# **Exploded view**



## Mechanical pa

Remark: Unless oth

POS.	SPARI
0001	
0002	X
0003	X
0004	X
0005	
0006	
0007	Χ
8000	Χ
0009	
0010	Χ
0011	X
0012	
0013	Χ
0014	
0016	
0017	
0018	
. 0019	
0020	
0021	
0022	
0023	
0024	
0025	Χ
0026	
0027	
0028	
0031	
0350	
1000	
1001	X
1002	X
1002	X
1003	Χ
1004	X
1005	X



# **Mechanical parts**

Remark: Unless otherwise noted, all parts are valid for both -/23 and -23S versions.

POS.	SPARE	CODE NUMBER	ARTICLE DESCRIPTION
0001			FRAME ASSY
0002	X	482236111069	FAN ASSY
0003	X	482253212988	FAN STRING
0004	X	482232012297	CABLE ASSY GPS-MODULE
0005			SCR PAN TAP ST ZN YE M2.5X6
0006			SCR PAN TAP ST ZN GN M2.5X5
0007	X	311234125030	COVER EMC
8000	X	311237425850	SCREENING DUST
0009			SCR PAN TAP ST ZN YE M2.5X6
0010	X	311231123690	COVER TOP
0011	X	311235825030	COVER BOTTOM ASSY
0012	•		HOLDER CSB
0013	X	482249271046	SPRING MOUNTING
0014			SCR CSK TORX TAP ST ZN M3X6
0016			SCR PAN TAP ST ZN YE M2.5X6
0017			BRACKET GYRO (ONLY 22SY591/23)
0018			SCR PAN TAP ST ZN YE M2.5X6
0019			HOLDER
0020			LABEL SEAL STANDARD
0021			LABEL
0022			LABEL FINAL CONTROL
0023			LABEL WHITE/GREEN
0024			LABEL SW LICENSE
0025	X	311235825970	COVER ASSY EMC
0026			LABEL PRODIS
0027			LABEL US MARKET SY500/23
0028			LABEL D18 BLUE (ONLY 22SY591/23S)
0031			LABEL WHITE/GREEN
0350			THERMOFOIL AVERY 105 UNIVERSAL
1000			PWB ASSY MAIN
1001	X	311232854960	UNIT ASSY FRONT
1002	X	482269110732	MODULE ASSY CDM-M2 2.3E PACKED (22SY591/23)
1002	X	311232903040	MODULE ASSY CDM-M2 8.3 PACKED (22SY591/23S)
1003	X	311232854310	GPS RECEIVER ASSY SHIELDED
1004	X	311232854940	GYRO ASSY 90DEG ( <u>ONLY</u> 22SY591/23S)
1005	X	311233854100	PWB ASSY CSB

# **Electrical parts**

Only parts with a code number are available as spares! Unless otherwise noted, all parts are valid for both -/23 and -/23S versions.

- · · · )  - ·		•			
<b>CONNI</b> 1000	ECTOR BLOCK 311233853190	PWB ASSY CONNECTOR BLOCK (22SY59			
1000	311233854430	PWB ASSY CONNECTOR BLOCK (22SY59	1/235)		
MAIN	PCR				
1001	242202511684	CON BM V 3P M 2.50 5483 B	2221		CER2 0805 X7R 16V 220N PM10 R
1002	482226511508	CON BM V 14P F 1.27 RD R	2222	202202900458	TANCAP SM B45A 20V 22U PM20 R
1002	482226510813	CON BM H 6P F 1.25 FFC 0.3 B	2223		CER2 0805 X7R 16V 47N PM10 R
1003	482226750872	CON BM V 8P F 1.27 215079 B	2224		CER2 1210 Y5V 10V 22U P8020 R
1005		NOT PRESENT	2225		CER2 1210 Y5V 10V 22U P8020 R
1005		NOT PRESENT	2226		CER2 1210 Y5V 10V 22U P8020 R
1007		FRAME EMC	2227		CER2 0603 X7R 50V 10N PM10 R
1018		NOT PRESENT	2228		CER2 0603 X7R 50V 1N PM10 R
1052	242254301071	RES XTL SM 16MHZ0 7P CX-49G R	2229		CER2 0603 X7R 50V 1N PM10 R
1200		NOT PRESENT	2230		CER2 1210 Y5V 16V 10U P8020 R
1201	212266200109	PTC SM 3426 15V 0R085 PM R	2231		CER2 0805 X7R 16V 47N PM10 R
1300	242202516597	CON BM V 30P M 2.54 MODU II B	2232		CER2 0805 X7R 16V 220N PM10 R
1301	**-	NOT PRESENT	2233	202202900458	TANCAP SM B45A 20V 22U PM20 R
1302		NOT PRESENT	2234		CER2 1210 Y5V 10V 22U P8020 R
1303	<del></del>	NOT PRESENT (22SY591/23)	2235		CER2 0603 X7R 50V 10N PM10 R
1303	482226531105	CON BM V 4P F 2.00 (22SY591/23S)	2236		CER1 0805 NP0 50V 560P PM5 R
1304		NOT PRESENT	2237		CER2 0603 X7R 50V 560P PM10 R
1411	<del></del>	NOT PRESENT	2238		CER2 1210 Y5V 16V 10U P8020 R
1650	242254301074	RES XTL SM 6MHZ75 7P CX-49G R	2241	<del></del>	NOT PRESENT CER2 0805 X7R 25V 100N PM10 R
1704		NOT PRESENT	2242		CER2 0603 X7R 50V 1N PM10 R
2000		CER2 0805 X7R 100V 4N7 PM10 R	2243		CER2 0805 X7R 16V 220N PM10 R
2001		NOT PRESENT	2250 2251		CER2 0805 X7R 16V 220N PM10 R
2002	<del></del>	NOT PRESENT	2251	202002490349	ELCAP SM VS 6V3 47U PM20 R
2003		CER2 0805 X7R 100V 4N7 PM10 R	2253	202002430343	CER2 0805 X7R 16V 220N PM10 R
2005		NOT PRESENT NOT PRESENT	2254		CER2 0805 X7R 50V 10N PM10 R
2006 2007		NOT PRESENT	2255		CER2 0805 X7R 25V 100N PM10 R
2007		NOT PRESENT	2256		CER2 0805 X7R 16V 220N PM10 R
2072		CER1 0805 NP0 50V 22P PM5 R	2257		CER2 0805 X7R 25V 100N PM10 R
2073		CER1 0805 NP0 50V 22P PM5 R	2258		CER2 0805 X7R 16V 220N PM10 R
2075		CER2 0805 X7R 16V 220N PM10 R	2259		CER2 0603 X7R 50V 1N PM10 R
2100		CER2 0805 X7R 16V 220N PM10 R	2261		CER2 0805 X7R 16V 470N PM20 R
2101		CER2 0805 X7R 16V 220N PM10 R	2262		CER2 0603 X7R 16V 100N PM10 R
2103		CER 1 0603 NP0 50V 27P PM5 R	2263	<del></del>	NOT PRESENT
2104		CER1 0603 NP0 50V 10P PM5 R	2264		NOT PRESENT
2105		CER1 0603 NP0 50V 10P PM5 R	2265		CER2 0805 X7R 16V 220N PM10 R
2106		CER2 0805 X7R 16V 220N PM10 R	2267		NOT PRESENT CER1 0805 NP0 50V 33P PM5 R
2107		CER2 0805 X7R 100V 4N7 PM10 R	2268 2269		CER2 0805 X7R 25V 100N PM10 R
2108		NOT PRESENT CER1 0603 NP0 50V 100P PM5 R	2209		CER2 0603 X7R 50V 1N PM10 R
2111		CERT 0603 NP0 50V 100P PM5 R	2271		CER2 0805 X7R 16V 220N PM10 R
2112 2113		CERT 0003 NP0 50V 100P PM5 R	2272		CER2 0603 X7R 16V 100N PM10 R
2113		CER2 0805 X7R 16V 220N PM10 R	2273		CER2 0805 X7R 25V 100N PM10 R
2118		CER2 0805 X7R 16V 220N PM10 R	2274		CER2 0603 X7R 16V 100 N PM10 R
2119		CER2 0603 X7R 50V 1N PM10 R	2275		CER2 0603 X7R 16V 100N PM10 R
2120		CER2 0603 X7R 50V 1N PM10 R	2276		CER2 0603 X7R 16V 100 N PM10 R
2121		CER1 0603 NP0 50V 27P PM5 R	2277		CER2 0603 X7R 16V 100 N PM10 R
2122		CER1 0603 NP0 50V 27P PM5 R	2278		CER2 0603 X7R 16V 100 N PM10 R
2123		CER1 0603 NP0 50V 27P PM5 R	2279		CER2 0603 X7R 16V 100 N PM10 R
2200	202002490702	ELCAP M 25V S 2200U PM20 B	2280		CER2 0805 X7R 16V 220 N PM10 R
2201		CER2 0805 X7R 100V 4N7 PM10 R	2281	<del></del>	NOT PRESENT
2202		CER2 1210 Y5V 10V 22U P8020 R	2282		CER2 0603 X7R 50V 1N PM10 R
2203	202002191487	ELCAP VZ 16V S 2200U PM20 B	2300		CER2 0805 X7R 25V 100 N PM10 R CER2 0805 X7R 16V 220 N PM10 R
2204		CER2 0805 X7R 16V 220N PM10 R	2301		CER2 0805 X7R 16V 220N PM10 R
2205	482212481061	ELCAP SM VS 6V3 22U PM20 R	2302		CER2 0805 X7R 25V 100N PM10 R
2206		CER2 0805 X7R 16V 220N PM10 R	2303 2304		CER2 0603 X7R 50V 1N PM10 R
2207	403343443404	CER2 0805 X7R 16V 220N PM10 R	2304		RST SM 0603 JUMP. MAX 0R05 R
2208	482212412194	ELCAP SM VS 50V 2U2 PM20 R	2308		CER2 0603 X7R 50V 10N PM10 R
2209	482212412194	CER2 0805 X7R 16V 220N PM10 R ELCAP SM VS 50V 2U2 PM20 R	2309		CER2 0805 X7R 50V 1N PM10 R
2210 2211	402414414174	CER2 0603 X7R 50V 1N PM10 R	2312		CER2 0805 X7R 16V 220N PM10 R
2211		CER2 1210 Y5V 10V 22U P8020 R	2312		CER2 0805 X7R 100V 4N7 PM10 R
2212		CER2 0805 X7R 16V 220N PM10 R	2314		CER1 0603 NP0 50V 47P PM5 R
2213		CER2 0603 X7R 10V 220N 1 M 10 R	2315		CER1 0603 NP0 50V 47P PM5 R
2215		CER2 0805 X7R 100V 4N7 PM10 R	2316		CER2 0603 X7R 50V 1N PM10 R
2216	482212481061	ELCAP SM VS 6V3 22U PM20 R	2317		CER2 0603 X7R 50V 1N PM10 R
2217		CER2 0805 X7R 16V 220N PM10 R	2318		CER2 0603 X7R 50V 1N PM10 R
2218		CER2 0805 X7R 16V 220N PM10 R	2319		CER2 0603 X7R 50V 1N PM10 R
2219		CER2 1210 Y5V 16V 10U P8020 R	2320		CER2 0805 X7R 25V 100N PM10 R
2220		CER2 1210 Y5V 16V 10U P8020 R	2321		CER2 0603 X7R 50V 1N PM10 R
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3113		RST SM 0805 RC11 470R PM5 R	3215		RST SM 0805 RC11 1K PM5 R
		RST SM 0805 RC11 470R PM5 R	3216		RST SM 0805 RC11 10K PM5 R
3114					
3115		RST SM 0805 RC11 470R PM5 R	3217		RST SM 0805 RC11 1K PM5 R
3116		RST SM 0805 RC12H 560R PM1 R	3218		RST SM 0805 RC11 10K PM5 R
3117		NOT PRESENT	3219		RST SM 0805 RC11 1K PM5 R
	<del></del>				RST SM 0805 RC11 470K PM5 R
3118		NOT PRESENT	3220		**
3119		NOT PRESENT	3221		RST SM 0805 RC11 2K7 PM5 R
		NOT PRESENT	3222		RST SM 0805 RC12H 1K8 PM1 R
3120					
3121		NOT PRESENT	3223		RST SM 0805 RC12H 5K1 PM1 R
3122		NOT PRESENT	3224		RST SM 0805 RC11 47R PM5 R
			3225		RST SM 0805 RC11 180R PM5 R
3123		NOT PRESENT			
3124		NOT PRESENT	3226		RST SM 0805 RC11 180R PM5 R
3125		NOT PRESENT	3227		RST SM 0805 RC11 4K7 PM5 R
	<del></del>	NOT PRESENT	3228		RST SM 0805 RC11 2K7 PM5 R
3126					
3127	<del></del>	NOT PRESENT	3229		RST SM 0805 RC12H 5K6 PM1 R
3128		NOT PRESENT	3230		RST SM 0805 RC12H 5K1 PM1 R
3129		NOT PRESENT	3231		RST SM 0805 RC11 180R PM5 R
					RST SM 0805 RC11 10K PM5 R
3130	<del></del>	NOT PRESENT	3232		
3131		NOT PRESENT	3233		RST SM 0805 RC11 10K PM5 R
3132		NOT PRESENT	3250		RST SM 0805 RC11 100K PM5 R
					RST SM 0805 RC12H 10K PM1 R
3133		RST SM 0805 RC11 100R PM5 R	3251		
3134		NOT PRESENT	3252		RST SM 0805 RC12H 10K PM1 R
3135		RST SM 0805 RC11 100R PM5 R	3253		RST SM 0805 RC12H 56K PM1 R
			3254		RST SM 0805 RC12H 10K PM1 R
3136		RST SM 0805 RC11 10K PM5 R			_
3137		RST SM 0805 RC11 100R PM5 R	3255		RST SM 0805 RC12H 10K PM1 R
3139		RST SM 0805 RC11 2K2 PM5 R	3256	232261513103	NTC SM 0805 0W21 10K PM5 R
					RST SM 0805 RC11 4K7 PM5 R
3141		RST SM 0805 RC11 180R PM5 R	3257		
3142		RST SM 0805 RC11 47R PM5 R	3259		RST SM 0805 RC11 4K7 PM5 R
3143		NOT PRESENT	3260		RST SM 0805 JUMP. MAX 0R05 R
			3261		RST SM 0805 RC11 4K7 PM5 R
3148		RST SM 0805 RC11 180R PM5 R			
3149		RST SM 0805 RC11 10K PM5 R	3262		RST SM 0805 RC11 10K PM5 R
3150		RST SM 0805 RC11 10K PM5 R	3263		RST SM 0805 RC11 10K PM5 R
		RST SM 0805 RC11 47R PM5 R	3264		RST SM 0805 RC11 10K PM5 R
3151					
3152		NOT PRESENT	3265		RST SM 0805 RC11 33K PM5 R
3161		RST SM 0805 RC11 47R PM5 R	3266		RST SM 0805 RC11 22K PM5 R
		RST SM 0805 RC11 10K PM5 R	3267		RST SM 0805 RC11 15K PM5 R
3162					
3163		RST SM 0805 RC11 2K2 PM5 R	3268		RST SM 0805 RC11 22K PM5 R
3164		NOT PRESENT	3269	<del></del>	NOT PRESENT
		RST SM 0805 RC12H 1K5 PM1 R	3270		RST SM 0805 RC12H 56K PM1 R
3167					
3168		RST SM 0805 RC12H 1K5 PM1 R	3271	<del></del>	NOT PRESENT
3169		RST SM 0805 RC12H 1K8 PM1 R	3273		RST SM 0805 RC11 100K PM5 R
		RST SM 0805 RC12H 1K PM1 R	3274		NOT PRESENT
3170					
3 <b>1</b> 71		RST SM 0805 RC12H 1K PM1 R	3275		RST SM 0805 RC11 22K PM5 R
3172		RST SM 0805 RC12H 1K PM1 R	3276		RST SM 0805 RC12H 56K PM1 R
		RST SM 0805 RC12H 1K8 PM1 R	3277		RST SM 0805 RC11 220K PM5 R
3173					
3174		RST SM 0805 RC12H 1K8 PM1 R	3278	<del></del>	NOT PRESENT
3175		RST SM 0805 RC12H 1K8 PM1 R	3279		RST SM 0805 RC11 22K PM5 R
		RST SM 0805 RC12H 2K2 PM1 R	3280		RST SM 0805 RC12H 56K PM1 R
3176					RST SM 0805 RC11 100K PM5 R
3177		RST SM 0805 RC12H 2K2 PM1 R	3281		
3178		RST SM 0805 RC12H 2K2 PM1 R	3282		RST SM 0805 RC11 10K PM5 R
3179		RST SM 0805 RC11 10R PM5 R	3283		RST SM 0805 RC11 10K PM5 R
					RST SM 0805 RC11 4K7 PM5 R
3180		RST SM 0805 RC11 10R PM5 R	3286		
3181		RST SM 0805 RC11 10R PM5 R	3287		RST SM 0805 RC11 100R PM5 R
3182		RST SM 0805 RC11 10R PM5 R	3288		RST SM 0805 RC11 10K PM5 R
			3290		RST SM 0805 RC11 10K PM5 R
3183		RST SM 0805 RC11 10R PM5 R			
3184		RST SM 0805 RC11 47R PM5 R	3291		RST SM 0805 RC11 4K7 PM5 R
3185		RST SM 0805 RC11 180R PM5 R	3300		NOT PRESENT
3186		RST SM 0805 RC11 180R PM5 R	3301		RST SM 0805 RC11 22K PM5 R
					RST SM 0805 RC11 22K PM5 R
3187		RST SM 0805 RC11 47R PM5 R	3302		
3188		RST SM 0805 RC11 180R PM5 R	3303		RST SM 0805 RC11 82K PM5 R
3189		RST SM 0805 RC11 180R PM5 R	3304		RST SM 0805 RC11 4K7 PM5 R
		RST SM 0805 RC11 47R PM5 R	3305		RST SM 0805 RC11 470K PM5 R
3190					
3191		RST SM 0805 RC11 180R PM5 R	3306		RST SM 0805 RC11 10K PM5 R
3192		RST SM 0805 RC11 180R PM5 R	3307		NOT PRESENT
		RST SM 0805 RC11 180R PM5 R	3308		RST SM 0805 RC11 22K PM5 R
3193					
3200		RST SM 0805 RC11 150K PM5 R	3309		RST SM 0805 RC11 22K PM5 R
3201		RST SM 0805 RC11 1K PM5 R	3310		NOT PRESENT
		RST SM 0805 RC11 1K PM5 R	3311		RST SM 0805 RC11 1K5 PM5 R
3202					
3203		RST SM 0805 RC11 1K PM5 R	3312		RST SM 0805 RC11 470K PM5 R
3204		RST SM 0805 RC11 4K7 PM5 R	3313		NOT PRESENT
3205		RST SM 0805 RC11 27K PM5 R	3314		RST SM 0805 RC11 22K PM5 R
3206		RST SM 0805 RC11 33K PM5 R	3315	<del></del>	NOT PRESENT
3207		RST SM 0805 RC11 27K PM5 R	3316		RST SM 0805 RC11 1K PIM5 R
		RST SM 0805 RC11 27K PM5 R	3317		NOT PRESENT
3208				•	
3209		NOT PRESENT	3318		RST SM 0805 RC11 10k PM5 R
3210		RST SM 0805 RC11 10K PM5 R	3319	<del></del>	NOT PRESENT
3211		RST SM 0805 RC11 10K PM5 R	3320		NOT PRESENT
			3320	-	
			7774		DCT CNA COCE DC11 224 DNAE D
3212		RST SM 0805 RC11 10K PM5 R	3321		RST SM 0805 RC11 22k PM5 R
3212			3321 3322		RST SM 0805 RC11 22k PM5 R RST SM 0805 RC11 22k PM5 R
3212 3213		RST SM 0805 RC11 10K PM5 R RST SM 0805 RC11 1K PM5 R	3322		RST SM 0805 RC11 22k PM5 R
3212		RST SM 0805 RC11 10K PM5 R			RST SM 0805 RC11 22K PM5 R RST SM 0805 RC11 22K PM5 R NOT PRESENT

3687

3800

3801

3802

3803

3804

3805

RST SM 0805 RC11 22K PM5 R

RST SM 0805 RC11 22K PM5 R

RST SM 0805 RC11 10K PM5 R

RST SM 0805 RC11 22K PM5 R

NOT PRESENT

**NOT PRESENT** 

RST SM 0805 RC11 470K PM5 R

3324

3325

3326

3327

3329

3330

3331

RST SM 0805 RC11 4K7 PM5 R

RST SM 0805 RC11 1K2 PM5 R

RST SM 0805 RC11 10K PM5 R